

518 Rec'd PCT/PTO 02 AUG 2001
09/890575

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Express Mail No. EL109505851US Attorney's Docket No. 991174 US
Deposited: August 1, 2001

Commissioner of Patents and Trademarks
Box: PCT
Washington, DC 20231

EXPRESS MAIL CERTIFICATE

I hereby certify that the attached correspondence, identified below, is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" under 35 U.S.C. 371 on the date indicated above and is addressed to the Commissioner for Patents and Trademarks, Washington, DC 20231.

By



Anita M. Auten

TRANSMITTAL OF INTERNATIONAL PATENT APPLICATION
FOR NATIONAL FILING UNDER 35 U.S.C. § 371
(Page 1 of 2)

Dear Sir:

Transmitted herewith for national filing in the US PTO under 35 U.S.C. § 371 of PCT International Patent Application No. PCT/SG99/00005, filed on February 4, 1999:

Inventor(s): Uppili SRIDHAR, Victor D. SAMPER, Pang Dow FOO,
Dirk WAGENAAR, Kay KRUPKA and Helmut SCHLAAK

For: MICRO-RELAY

Enclosed are papers, identified as follows:

- ☒ Copy of PCT International Publication
- ☒ Copy of PCT International Preliminary Examination Report
- ☒ Copy of PCT Notification of the Recording of a Change
- ☐ Declaration or Oath and Petition, Power of Attorney
- ☒ To follow if this line is checked
- ☒ Information Disclosure Statement
(reference(s) not cited in PCT International Search Report are enclosed, if any)

Assignment(s) is/are:

- ☐ enclosed, signed by inventor(s) to employer(s), with a separate cover sheet; or
- ☒ to follow.

09/890575 201120 52506860

09/890575

JC95 Rec'd PCT/PTO 0 2 AUG 2001

Attorney's Docket No. 991174 US
Inventor: Uppili SRIDHAR et al.

TRANSMITTAL OF INTERNATIONAL PATENT APPLICATION
FOR NATIONAL FILING UNDER 35 U.S.C. § 371
(Page 2 of 2)

A filing fee is calculated based on the claims remaining in the application after entry of the preliminary amendment, if applicable,

Basic Fee under 1.492 (a) (3)						= \$ 1000
Total Claims	60	minus 20	= * 40	X	\$ 18	= \$ 720
Independent	3	minus 3	= *	X	\$ 80	= \$
Fee for Multiple dependent claims						\$ 270 = \$


Filing fee, Charge to Deposit Account 23-1950 \$ 1720

Authorization to Charge Deposit Account 23-1950: The Commissioner is authorized to charge any fee or credit any overpayment to deposit account 23-1950 in conjunction with carrying out this or any other communication filed in the present invention.

Please direct all future correspondence to:

Tyco Technology Resources
4550 New Linden Hill Road, Suite 450
Wilmington, DE 19808
USA

Respectfully submitted,
Uppili SRIDHAR et al.
Applicant



Joseph A. Tessari
Registration No. 32177
Attorney for Applicant
Tyco Technology Resources
4550 New Linden Hill Road
Suite 450
Wilmington, Delaware 19808
Phone (302) 633-3566
Fax (302) 633-2776

09/890575 034

MICRO-RELAYField of the Invention

The invention relates generally to microstructures and
5 particularly to relays and switches or valves formed from
microstructures.

Background of the Invention

Electrical relays are widely used in various
10 applications. Such applications include, for example, the
selection of different electrical paths or the opening or
closing of an electrical circuit.

Typically, relays include a coil and mechanical
components for engaging and disengaging a pair of contacts.
15 Upon energizing the coil, an electromagnetic field is
generated to engage the contacts, forming the electrical
connection.

There is now a demand for the miniaturization of
consumer goods (e.g., electronic and telecommunication
20 products) or electronic equipments (e.g., automated test
equipment), which creates a corresponding need to reduce
the size of relays. However, conventional
electromechanical relays do not lend easily to
miniaturization. For example, there is a limit as to the

14/PRTS

09/890575

JCO5 Rec'd PCT/PTO 0 2 AUG 2001

WO 00/46852

PCT/SG99/00005

2

size of coils that can be reduced. Such limitations reduce the extent of miniaturization of products using relays.

The above discussion evidences a need to provide a relay design that allows for further miniaturization.

5

Description of the Drawings

Figs. 1a-1b are cross-sectional and plan views of a relay in accordance with one embodiment of the invention;

10 Figs. 2a-2b are cross-sectional and plan views of a relay in accordance with another embodiment of the invention;

Figs. 3-8 are three-dimensional views depicting a process of fabricating a relay in accordance with one embodiment of the invention;

15 Figs. 9-10 are three-dimensional views depicting an alternative process of fabricating a relay; and

Figs. 11-12 are three-dimensional views depicting another process of fabricating a relay.

Summary of the Invention

20 The invention relates to a microstructure relay. The relay comprises a body having upper and lower portions. A support member having a first end fixed to the body to form a cantilever is provided. The upper surface of the support

09890575 024100

member and a lower surface of the upper portion of the body form a cavity. A first contact region is located on the upper surface at a second end of the support member. The first contact region includes a first contact that is
5 electrically coupled to a second contact when the support member is moved upward toward the lower surface.

In accordance with one embodiment of the invention, the upper portion of the body is formed on the lower portion of the body. This requires only a single substrate
10 to fabricate the relay, avoiding the need to bond upper and lower portions of the relay together.

In accordance with another embodiment of the invention, the relay comprises an s-shaped support member to provide over-travel. First and second stress layers are
15 used to form the s-shape support member. The first stress layer induces compressive stress to cause the support member to bend away from the upper portion of the relay, and a second layer is provided to induce the first contact region to bend in the opposite direction toward the upper
20 portion of the relay. The first stress layer can be patterned to be on the support member except in the first contact region. The stress layers, in one embodiment, comprise high temperature materials formed using semiconductor processing techniques.

2016-05-25 09:55:55

A method for fabricating microstructures is provided. The method includes the use of electrochemical etching to form microstructures. Electrochemical etching relies on an etch stop which, for example, may be a p-n junction. The electrochemical etch etches, for example, p-doped portion or region and stops on n-doped region. In one embodiment, a heavily doped p-type region is used. The use of a heavily doped p-type region enables the etch to form holes or slots having lateral dimensions less than 250 μm .

10

Description of the Invention

The invention relates to the fabrication of microstructures. In particular, the invention relates to forming relays from microstructures. The use of microstructures enables the fabrication of miniature relays.

Figs. 1a-b are cross-sectional and plan views, respectively, of a micro-relay in accordance with one embodiment of the invention. As shown, the relay comprises a body 110. The body includes a support member 160. The support member is supported at a first end 163 by the body, forming a cantilever. A first contact region 133 is

0989055 0340
20110525 1506560

provided at about a second end 165 of the support member.

The first contact region comprises a first contact 131.

A gap separates a top surface 168 of the support member and a surface 109 of the body, forming a cavity 121.

5 The cavity allows the support member to pivot about the first end 163. The surface 109 comprises a contact region 134 on which a second contact 130 is provided. The second contact is usually referred to as the counter contact. The first and second contacts are positioned such that they
10 contact each other when the support member is pivoted upward toward surface 109. The dimensions of the contacts depend on design parameters and can be varied as required. Typically, the height of the contacts is about 2-10 microns (μm).

15 An electrostatic force is used to move or pivot the support member toward surface 109 to engage the first and second contacts. The relay incorporates the use of electrodes having opposite charges or opposite dipole layers applied thereto to generate the electrostatic force.

20 In accordance with one embodiment of the invention, a first electrode 141 is located on the surface 168 of the support member and a second electrode 140 is located on the surface 109 of the body. The electrodes are isolated from

201120 5250880

the contacts. The first and second electrodes, as shown, are aligned with each other to produce an electrostatic force when opposite charges are applied to the electrodes. A dielectric layer 145 is provided either on the first or
5 the second electrode to prevent shorting when the support member is moved upward toward surface 109. Providing a dielectric layer on both electrodes is also useful.

In effect, the electrodes and dielectric layer form a capacitor for generating the electrostatic force. The
10 amount of electrostatic force generated depends on, for example, the surface area of the electrodes, the voltage applied to the electrodes (pull-in voltage), and the distance between the electrodes. The applied voltage needed to create a sufficient electrostatic force depends
15 on design parameters such as the stiffness of the support member and the contact force desired.

In one embodiment, the body includes upper and lower portions 105 and 106. The lower portion 106 preferably comprises silicon. Other crystalline materials such as
20 gallium arsenide are also useful. Materials such as quartz, ceramics, glass, silicate glass such as borosilicate glass, Pyrex®, or other materials that can provide support for the relay components can also be used. The upper portion 105, in one embodiment, comprises nickel

or a nickel alloy such as nickel-iron. Other materials, including gold, alloys, plastic, epoxy, and materials that can adequately support the relay components as needed, can also be used.

- 5 In accordance with one embodiment of the invention, the upper portion is formed or deposited on the lower portion. This advantageously avoids the need to bond the upper and lower portions together as required by conventional two wafer approaches, improving reliability
- 10 and lowering manufacturing cost.

- In the upper portion, surface 109 is provided on which the second contact 130 is located. The second contact is formed in a second contact region 134 on surface 109. As shown, a surface 108 in the second contact region is
- 15 recessed from surface 109 to produce a separate contact region for the contact. A second contact region that is not recessed from surface 109 can also be used. The contact comprises a conductive material. The contact material should have low contact resistance, good thermal
- 20 conductance, and hardness to resist sticking over the life time of the relay. Other desirable characteristics of the contact material includes high resistance against light arcing, high light arcing voltage, low stress, and low or no cold welding effects. In one embodiment, the contact

09890575-024102

comprises gold. For example, including gold such as gold-palladium, gold-nickel, and gold-cobalt or alloys including silver are also useful. Other conductive materials that provide good contact characteristics are also useful to
5 form the contact.

Supported by the body at the first end 163 is the support member 160. The first contact region 133 comprising the first contact 131 is located at about the second end 165 of the support member. It is not necessary
10 to use the same material to form both the first and second contacts. When the support member is moved upward, the contacts make contact with each other.

In one embodiment, the support member is part of the lower portion of the body. Such a support member comprises
15 the same material as the lower portion of the body. In a preferred embodiment, the support member comprises single crystalline silicon. Other single crystalline materials are also useful for forming the lower portion and support. Materials that comprise good spring characteristics such as
20 low fatigue and creep are also useful.

Providing a support member that is not part of the lower portion of the body is also useful. With such a design, materials which are different from those comprising the lower portion of the body can be used for the support

09890575 034400

member. Materials that exhibit good spring characteristics are useful to form the support member. In one embodiment, the support member comprises polysilicon (poly). The poly can be deposited either as poly or as amorphous silicon and
5 recrystallized to form poly. The use of epitaxially grown single crystalline material to form the support member is also useful. Nickel, nickel alloy such as nickel-iron, metals, or metal alloys that show good spring characteristics can also be used to form the support
10 member.

The second electrode 140 is provided on the surface 109 of the upper portion of the body, and the first electrode 141 is provided on the surface 168 of the support member. The electrodes comprise a conductive material.
15 Various conductive or semi-conductive materials can be used to form the electrodes. To simplify processing, the electrodes can be formed from the same material used to form the contacts. The thickness of the electrodes is not critical. However, the first electrode should be
20 relatively thin to reduce the impact of mechanical stress induced by the electrode on the support member. In one embodiment, the first electrode should be as thin as possible to minimize the stress on the support member. The electrodes are isolated or separated from the contacts.

support member to move upward. This engages the first and second contacts, closing the load circuit.

Illustratively, the first and second contacts are coupled to a respective portion of the load circuit. The leadout coupling the first contact to pad is provided over the first electrode. To isolate the electrode from the lead out, the first electrode is divided into two portions 141a and 141b. As such, leadouts are provided to couple the electrodes to a common pad. Alternatively, a dielectric layer can be provided under the lead out to provide electrical isolation between the electrode and the lead out.

In another embodiment, the first contact is not coupled to a portion of the load circuit. Instead, first and second counter contacts are provided, coupling to respective portions of the load circuit. The respective portions of the load circuit are electrically coupled when the support member is moved upward to make contact with first and second counter contacts. Such a design does not require a leadout and pad for the first contact, eliminating the need to divide the first electrode into two portions or to provide a dielectric layer between the leadout and electrode forming a bridge contact. Additional contacts or alternative contact configurations are also

useful. For example, multiple sets of contacts can also be provided for driving a plurality of load circuits.

A sufficiently high force between the contacts is desired in order to have low contact resistance. The low
5 contact resistance should be maintained over the lifetime of the relay, in view of contact wear. In one embodiment, the relay comprises an over-travel to produce high contact force between the contacts. The over-travel is the distance that one contact travels beyond the surface of the
10 other contact in the absence of the other contact when the support member is moved upward to engage the contacts. Ideally, the over-travel adequately maintains the high contact force to produce a low stable contact resistance over the lifetime of the relay. The amount of over-travel
15 provided depends on, for example, the wear on the contact and contact characteristics. The over-travel, for example, is about 5 μ m. Other over-travel distances are also useful.

In one embodiment, the over-travel is incorporated in the support member. The over-travel results from an s-
20 shaped support member (as shown by the dotted lines). The s-shape is achieved by inducing the main portion of the support member to deflect or bend about an axis in a first direction and inducing the end 165 to bend in the opposite

20120525 09:40

direction. To create the s-shape, the main portion of the support member is induced to deflect downward away from the surface 109 while the end 165 is induced to bend upward toward the surface 109.

5 In one embodiment, over-travel is provided by inducing the main portion of the support member to deflect downward away from surface 109 while inducing the first contact region to deflect in the opposite direction of the main portion. In a preferred embodiment, the first contact
10 region is isolated from remaining end portions 165a and 165b of the support member. Isolating the first contact region forms a cantilever within the support member. Such a design is advantageous as it increases the contact force applied between the contacts.

15 In one embodiment, a first stress layer 180 is provided on the surface 168 of the support member. The first stress layer induces a compressive stress on the support member, causing it to bend or arc downward away from the first electrode. Preferably, the first stress
20 layer induces an intrinsic compressive stress of the support member. The second stress layer 181 is provided in the first contact region at about the second end of the support member, inducing a tensile stress to cause the first contact region to arc upward toward the surface 109.

0980575-03402

Preferably, the second stress layer induces an intrinsic tensile stress on the first contact region of the support member. This combination of the tensile and compressive stresses creates the s-shaped support member. The amount
5 of bending depends on the material and its thickness. The material and thickness of the stress-inducing layers are selected to create the shape that produces the desired amount of over-travel.

In an alternative embodiment, the first stress-
10 inducing layer is provided on the surface 168 of the support member except in the first contact region. The first layer induces a compressive stress on the support member, causing it to bend or arc downward away from the first electrode. The second layer is provided in the first
15 contact region at about the second end of the support member, inducing a tensile stress to cause the first contact region to arc upward toward the surface 109.

The stress-inducing layers preferably comprise relatively high temperature formed materials such as those
20 used in semiconductor processing. Such materials include, for example, silicon oxide, silicon nitride, poly, and epitaxially grown silicon. The materials can be doped or undoped.

09890575-03400

The high temperature formed materials comprise stable mechanical properties (e.g., intrinsic stress, low fatigue and creep) and are unaffected by other lower temperature processes used in forming the relay. The stability and
5 repeatability of high temperature formed materials provide an excellent stress-inducing layer, enabling the support member to maintain its shape over time and various operating temperatures. This improves the reliability of the relay. Furthermore, the use of such materials reduce
10 production cost.

In one embodiment, the support member comprises silicon. The first stress-inducing layer comprises silicon oxide to induce compressive stress on the support member. Other materials that induce compressive stress on the
15 support member, such as doped or undoped poly, are also useful. To induce tensile stress on the support member, the second stress-inducing layer comprises silicon nitride (Si_3N_4). Other materials that induce tensile stress on the support member are also useful.

20 Alternatively, metals or other materials formed by lower temperature processes to either induce compressive or tensile stress on the support member can also be used as stress-inducing layers.

20170525093600

As described, non-symmetrical material combinations having different stresses at about the bending axis are used to induce bending of a composite structure. The stress induced includes different components, such as

5 intrinsic stress and stress due to thermal expansion. The different stress components can be affected by the thickness of the stress-inducing layer. Increasing or decreasing the thickness increases or decreases the magnitude of the intrinsic stress component on the support

10 member. The thermal component of stress, which depends on a material's thermal coefficient of expansion (TCE), is also affected by temperature. The stress due to thermal expansion changes as the stress-inducing layer expands or contracts due to temperature variations.

15 In some applications, particular with devices that operate over a wide operating temperature range, it may be desirable to have a support member that maintains a stable shape over the contemplated operating temperature range. A stable support member can be maintained over a broad

20 temperature range by effectively reducing or minimizing the thermal component of stress. Different techniques can be employed to reduce the stress variations from TCE.

In one embodiment of the invention, variations of stress due to TCE is reduced or minimized by employing

0960575-021102
20171220 15:25:06

stress-inducing layers comprising TCEs that are similar to that of the support member. Closely matching the TCEs of the different materials enables the support member to maintain a stable shape over the relay's contemplated
5 operating temperature range.

Alternatively, variations in the stress induced by the stress layer due to thermal expansion can be avoided by using thin stress-inducing layers. Reducing the thickness of the stress layers decreases their thermal stress
10 component. Using a thin stress layer may require the material to having a higher intrinsic stress component to produce the desired bending on the support member.

In yet another embodiment, a compensation layer is used to reduce or minimize the effects of a mismatch
15 between the TCEs of the different layers. In one embodiment, the compensation layer is provided in contact with the stress-inducing layer. In one embodiment, the compensation layer is provided between the support member and stress-inducing layer. The compensation layer
20 comprises a TCE that is similar but opposite (compressive instead or tensile or vice-versa) to that of the stress layer. Providing a compensation layer having the opposite TCE as the TCE of the stress-inducing layer cancels out the effects of the TCE mismatch.

09890575 034400

In the case where the support member is not a part of the lower portion of the body, the compensation layer can be provided on the opposite surface on which the stress-inducing layer is located. In one embodiment, the

5 compensation layer is provided on the lower surface of the support member while the stress-inducing layer is on the upper surface of the support member. The compensation layer comprises a TCE that is similar to that of the stress-inducing layer. Providing a compensation layer

10 having a TCE that closely matches the TCE of the stress-inducing layer on the opposite surface of the support member cancels out effects of the TCE mismatch between the stress-inducing layer and support member.

The intrinsic stress in the compensation layer should

15 be much lower than the intrinsic stress in the stress layer in order to reduce or minimize compensation layer's influence on the support member.

To ensure that the over-travel of the support member is not hindered, an over-travel region 170 can be provided

20 on the surface 109. The over-travel region is created by recessing a surface 171 from surface 109. The over-travel region enables the upward bend of the support member not to be inhibited by the surface 109. The dimensions of the over-travel region should be sufficient to accommodate the

09890575 024400

over-travel of the support member. The surface area of the over-travel region should be larger than the surface area of the over-travel portion of the support member. The depth or height of the over-travel region is, for example, equal to the sum of the heights of the first and second contacts.

As shown from the plan view of Fig. 1b, the support member comprises a cantilever which is somewhat rectangular in shape. Other shapes, such as circular or elliptical shapes, are also useful. The size or surface area of the support member can be designed to take into account the size requirements of the electrodes in order to generate the force needed to move the support member. The surface area of the support member can be, for example, 1500 μm by 1200 μm .

Figs. 2a-b are cross-sectional and plan views of an alternative embodiment of the invention. As shown, the relay incorporates a dual support member design comprising first and second support members 260 and 270. The use of dual support members advantageously avoids the need of a second stress layer (tensile stress inducing layer) to produce the over-travel. The relay comprises a body 210. The first support member 260 is supported by the body at a

first end 262, forming a cantilever. Likewise, the second support member comprises a cantilever having a first end 272 supported by the body. The support members are effectively in about a same plane. A gap separates the support members and a surface 209 of the body, forming a cavity 221.

In one embodiment, the body comprises lower and upper portions 205 and 206. The lower portion comprises silicon. Other crystalline materials such as gallium arsenide are also useful. Materials such as quartz, ceramics, glass, silicate glass such as borosilicate glass, Pyrex[®], or materials that can provide support for the relay components can also be used. The upper portion 105, in one embodiment, comprises nickel. Other materials, including gold, plastic, epoxy, and materials that can adequately support the relay components as needed, can be used.

Providing a support member that is not part of the lower portion of the body is also useful. With such a design, materials which are different from those comprising the lower portion of the body can be used for the support member. Materials that exhibit good spring characteristics (e.g., low fatigue and creep) are useful for the support member. A support member comprising poly, for example, can be useful. The poly can be deposited either as poly or as

amorphous silicon and recrystallized to form poly. The use of epitaxially grown single crystalline material to form the support member is also useful. Nickel, nickel alloy such as nickel-iron, metals or other alloys that have the
5 desired characteristics can also be used to form the support member.

A first contact 231 is located at about a second end 265 of the first support member, and a second contact 230 is located at about a second end 275 of the second support
10 member. The contacts are positioned such that when the first support member moves upward toward the surface 209, they come into contact with each other.

In accordance with one embodiment of the invention, the second contact is the counter contact, and the first
15 contact is moved upward to contact the second contact. The length of the second support member is relatively short to provide sufficient stiffness in order to produce the desired counter-force on the first contact when the contacts are engaged.

20 The first contact extends beyond the first support member to ensure that contact is made with the second contact when the second member is moved upward toward surface 209. In one embodiment, the portion of the contact that extends beyond the first support member is in a

09890575-024400

different plane with respect to the portion of the contact on the support member. In one embodiment, the portion of the contact that extends beyond the support member is in a plane elevated above the plane of the portion of the

5 contact on the support member. This provides for separation of the contacts when they are not engaged.

A first electrode 241 is located on surface 268 of the first support member, and a second electrode 240 is located on surface 209. A dielectric layer 245 is provided over

10 either the first or second electrode. Providing a dielectric layer over both electrodes is also useful. In one embodiment, the dielectric layer is provided over the second electrode. Upon the application of opposite charges to the first and second electrodes, the first support

15 member moves upward toward the second electrode to create electrical coupling between the contacts.

Over-travel can be incorporated into the relay design. In one embodiment, over-travel is provided by arcing the first and second members downward away from surface 209 (as

20 depicted by the dotted lines). The downward bend is incorporated by inducing compressive stress on the support members. As previously described, compressive stress is induced by the use of a compressive stress-inducing layer 281.

09890575.021103

Providing an s-shaped first support member to produce over-travel is also useful. The s-shaped support member is created using a compressive and tensile stress-inducing layers as described in Figs. 1a-b.

5 In one embodiment, an over-travel region 290 is provided. The over-travel region ensures that there is a sufficient gap between the counter contact 230 and end 265 to prevent obstructing over-travel of the support member 260.

10 Figs. 3-8 show the process for forming a relay in accordance with one embodiment of the invention. The figures provide a cross-section of a 3-dimensional view of the process. The cross-section is taken along an axis at about half of the support member. The other half of the
15 support member is typically, but not necessarily, symmetrical with the half as shown.

In accordance with one embodiment of the invention, electrochemical etch (ECE) techniques are used to form the support member. ECE techniques employ the use of
20 oppositely doped regions and the application of a charge. In one embodiment, the ECE etches the p-typed doped regions and passivates on the n-doped regions. As such, the n-doped regions effectively serve as an etch stop. Using ECE

09390575.02402

Referring to Figs. 3, a substrate 301 is provided. The substrate preferably comprises silicon such as a silicon wafer. Other crystalline substrates such as gallium arsenide are also useful. Substrates comprising materials such as quartz, ceramics, glass, silicate glass such as borosilicate glass (e.g. PYREX®), or materials that can provide support for the relay components can also be used.

In one embodiment, KOH is employed as the ECE etchant to etch silicon. KOH preferably etches silicon in the 100 plane selective to the 111 plane (i.e., a much higher etch rate in the 100 plane with respect to other planes). To accommodate the etch characteristics of the etchant, the substrate is oriented in the 100 plane. Other crystal orientations may also be useful.

The substrate includes a first doped region 305 comprising dopants of a first type. In one embodiment, the

first doped region comprises p-type dopants, such as boron (B). A second doped region 307 comprising dopants of a second type is formed on the surface of the substrate. In one embodiment, the second doped region comprises n-type dopants, such as arsenic (As) or phosphorus (P), in order to form an n-doped region. Using n-type dopants to form the first doped region and p-type dopants to form the second doped region is also useful.

The pattern or design of the second doped region defines the shape of the support member 360. As shown, the n-doped region is in the shape of a cantilever to serve as the support member. In one embodiment, the cantilever comprises edges that are perpendicular with respect to each other. Other cantilever shapes are also useful. For example, edges having angles other than 90° with respect to each other or curved edges are also useful. The depth of the second doped region defines the thickness of the support member. A subsequent ECE process selectively removes the first doped region 305 to form the support member.

The dimension of the support member determines its stiffness. The stiffness of the support member should enable the contacts to disengage or release when the voltage applied to the electrodes drops below the specified

5 Other dimensions are also useful, depending on design specifications such as the pull-in and drop-out voltages.

The use of a first doped region that is heavily doped provides an improvement over conventional ECE techniques, which use a lightly doped region. The lightly doped region results in a substrate having a resistivity of about 6-9 $\text{m}\Omega\cdot\text{cm}$. At such resistivity, ECE can only form holes or slots of greater than 250 μm . Increasing the dopant concentration to decrease resistivity to less than 6 $\text{m}\Omega\cdot\text{cm}$ enables the formation of holes or slots less than 250 μm . Increasing the dopant concentration to result in a substrate having a resistivity of about 50 $\text{m}\Omega\cdot\text{cm}$ enables the formation of significantly smaller features, for

example, less than 60 μm , preferably about 30 μm . Smaller features advantageously allow for smaller relay structures.

In one embodiment, a p-doped substrate having the desired dopant concentration is used to provide the p-doped region. Alternatively, p-type dopants are implanted and/or diffused into the substrate to form the p-doped region 305.

An n-doped region 307 is formed on the surface of the substrate. The n-doped region is formed by, for example, implanting dopants such as As or P, into selected regions of the substrate. The dopant concentration of the n-doped region should be sufficient to form a proper p-n junction with the p-doped region. Typically, the dopant concentration of the n-doped region is about $10^{18} - 10^{20}$ atoms/ cm^3 . In one embodiment, a POCl_3 source is used to selectively deposit P dopants into the substrate to form the n-doped region 307. A diffusion mask comprising, for example, silicon oxide (SiO_2) is used to protect certain regions of the substrate from being diffused with dopants while allowing dopants to enter the unprotected regions of the substrate. Alternatively, the dopants can be selectively implanted into the substrate.

In one embodiment, a first contact region 368 in which a first contact is formed is defined at about an end 365 of the support member. The first contact region, in one embodiment, forms a cantilever within the support member. The cantilever is formed by including slots 309 in the implant mask used to define the support member. The slots form a cantilever which can be induced to bend upward and have sufficient stiffness to increase the contact force between contacts. The length of the slots, for example is about $\frac{1}{3}$ - $\frac{1}{2}$ of the length of the support member. In one embodiment, the length of the slots is about 500 μm .

Where the first doped region is formed by implanting and/or diffusing dopants into the substrate, the depth of the second doped region should be less than the depth of the first doped region to ensure correct formation of the support member by ECE.

After the second doped region is formed, the implant mask is removed by wet etching the oxide selective to silicon.

In one embodiment of the invention, an s-shaped support member is employed to provide over-travel. To form the s-shaped support member, first and second stress layers that induce tensile and compressive stresses are deposited

on surface of the substrate. The stress layers preferably comprise high temperature formed materials. The high temperature materials are formed by semiconductor processing such as thermal oxidation in either dry or wet ambient and different types of chemical vapor deposition (CVD) processes such as low pressure CVD (LPCVD), high density CVD, or plasma enhanced CVD (PECVD). Such materials are desirable as they have very stable electrical and mechanical properties. For example, the intrinsic stress characteristics of the high temperature formed materials are very stable and are unaffected by the relatively lower temperature processes used to form other features of the relay. This results in a support member with a stable bend or shape. Lower temperature formed materials that are stable enough could also be used.

Referring to Figs. 4, a first stress layer is deposited on the surface of the substrate. The first stress layer induces compressive stress on the support member. The compressive stress causes the support member to arc downward.

In one embodiment, the first stress layer comprises silicon oxide (SiO_2). The thickness of the first stress layer induces the support member to bend as desired. Typically, the thickness of the SiO_2 is about 4000 - 5000Å.

The thickness of the stress layer can be varied, depending on design specifications such as over-travel and stiffness of the support member. In one embodiment, the SiO_2 is grown by thermal oxidation. The thermal oxidation can be performed in an oxygenated ambient. CVD techniques are also useful to form the first stress layer.

Alternatively, the first stress layer can comprise poly. The poly can be doped or undoped. The poly can be deposited as poly or as amorphous silicon which is subsequently recrystallized to form poly. Various CVD techniques can be used to deposit the poly. Relatively stable materials that are formed at lower temperatures may also be used. Other materials that are stable enough and induce tensile stress on the support member are also useful.

A second stress layer 472 is formed over the first stress layer. The second stress layer induces a compressive stress on the support member. In one embodiment, the second stress layer comprises Si_3N_4 . The Si_3N_4 is deposited by, for example, LPCVD. Other techniques for forming the second stress layer are also useful. The thickness of the second stress layer causes the support member to bend upward as desired. Typically, the thickness of the second stress layer is about 1000 - 2000Å. The

5

10

20

gold palladium, or gold-cobalt. Other conductive materials, such as silver, or silver alloys, are also useful.

To ensure adhesion of the conductive layer to the surface of the substrate, an adhesion layer can be deposited on the substrate surface prior to the formation of the conductive layer. In one embodiment, the adhesion layer comprises titanium. The titanium layer can also serve as a barrier layer to reduce inter-diffusion. Other materials that promote adhesion of the conductive material and support member, such as chromium, are also useful to serve as the adhesion layer. The adhesion is deposited by, for example, sputtering or evaporation. Other deposition techniques are also useful.

After formation of the conductive and adhesion layers, they are patterned to form a first electrode 541 on the support member. In one embodiment, the conductive layer is patterned, leaving the conductive layer on the support member except the first contact region. To reduce the stress induced by the electrode on the support member, it should be thin. In one embodiment, the thickness of the electrode is about 75nm.

A first contact 531 is formed in the first contact region. The contact comprises a conductive material such

as gold. Alloys comprising gold such as gold-palladium, gold-nickel, and gold-cobalt or alloys comprising silver are also useful. Other materials that provide good contact characteristics can also be used. An adhesion layer such
5 as titanium or chromium can be used to ensure adhesion of the contact material to the support member. The contact and adhesion layer are formed using conventional techniques such as, for example, sputtering, physical vapor deposition, electroplating, or other techniques.

10 In one embodiment, a pad and a leadout which connects the pad to the contact are also provided. The pad and leadout are, for example, formed from the same process as forming the contact. A leadout region is provided during the formation of the first electrode to accommodate the
15 leadout. The leadout region, for example, separates the first electrode into first and second subsections.

In one embodiment, the first contact is formed using electroplating techniques. Such techniques use a seed layer on which the contact material is plated. The seed
20 layer comprises a conductive material used to form the contact. Other types of materials that facilitate plating of the contact material can also be used. Various techniques, such as sputtering or evaporation, can be used to deposit the seed layer. An adhesion layer, for example

09890575 031103 2012052505860

titanium or chromium, may be provided to promote adhesion of the seed layer.

The adhesion and seed layers (adhesion/seed layer), in one embodiment, also serve as the first electrode. To
5 reduce the stress effects on the support member by the adhesion and seed layers, they should be relatively thin. To minimize the stress effects on the support member, the adhesion/seed layer should be as thin as possible. In one
10 embodiment, the adhesion layer is about 25 nm thick and the seed layer is about 50 nm thick, producing an adhesion/seed layer of about 75 nm thick.

In one embodiment, a mask is provided to selectively electroplate the conductive material in the first contact region to form the first contact. The mask, which may
15 comprise resist, exposes the seed layer in the region where the first contact is to be formed. The conductive material is plated onto the exposed portions of the seed layer to form the first contact. The dimensions of the contact should be adequate to handle the specified load current.
20 The height, for example, is about 2.5 μm .

The mask is removed after formation of the contact, exposing the seed/adhesion layer. An etch selectively removes portions of the seed/adhesion layer to form the

09890575.031400
"000000" 54500860

5 In one embodiment, the etch mask for forming the first electrode need not protect the contact. This results in a small amount of the surface of the contact and leadout being removed as the seed/adhesion layer is patterned. However, since contact is much thicker than the seed/adhesion layer, a sufficient amount should remain to
10 serve as the contact.

A dielectric layer can be provided underneath the lead
20 out for the contact. The dielectric layer provides
electrical insulation between the lead out and the
electrode, eliminating the need to pattern the electrode
into two subsections.

In an alternative embodiment, the conductive layer is electroplated onto the seed/adhesion layer. Also, the conductive layer can be deposited on an adhesion layer by, for example, sputtering or physical vapor deposition or
5 other deposition process that does not require the use of a seed layer. The conductive layer is subsequently masked and etched to form the contact and, if applicable, the leadout and pad. Forming the contact by masking and etching requires the deposition of an addition conductive
10 layer to serve as the electrode.

Referring to Fig. 6, an over-travel region is formed to ensure that the over-travel of the s-shaped support member is not hindered. To form the over-travel region, a first sacrificial layer 630 is deposited on the substrate.
15 The thickness of the sacrificial layer determines the depth of the over-travel region. The thickness is sufficient to accommodate the over-travel of the support member. The thickness of the sacrificial layer is, for example about 3-3.5 μm for a contact height of 2.5 μm .

20 The sacrificial layer comprises a material that can be etched selectively to other relay materials. Preferably, the sacrificial layer comprises a material that can be etched quickly and easily without effectively removing

other relay materials. In one embodiment, the first sacrificial layer comprises copper. Aluminum, titanium, zirconium, iron, polyimide or other materials that can be etched selective to other relay materials are also useful.

5 The use of SiO_2 as a sacrificial layer can also be useful, particularly in the case of a nickel spring using Si_3N_4 and poly as stress layers. The copper is deposited by, for example, physical vapor deposition or sputtering. Other deposition techniques, depending on the sacrificial

10 material, are also useful.

Conventional mask and etch processes are used to pattern the sacrificial layer to define the area of the over-travel region and to provide an opening 640 to the contact. The dimension of the over-travel region should be

15 sufficient to ensure that the over-travel of the support member is not hindered. The opening 640 defines the portion of the second contact that comes into contact with the first contact when the contacts are engaged.

In cases where the conductive and the sacrificial

20 materials interact to degrade the contact characteristics of the conductive layer by interdiffusion, a barrier layer can be provided between the two layers. Preferably, the barrier layer comprises a material that prevents interdiffusion between the conductive and sacrificial

09890575, 024403

materials and can be removed selective to other relay materials. The barrier layer, for example, comprises titanium, chromium, tungsten, or palladium.

A second sacrificial layer 631 is deposited over the surface. The second sacrificial layer comprises a material that can be etched selectively to other relay materials. Preferably, the second sacrificial layer comprises a material that can be etched quickly and easily without removing other relay materials. In one embodiment, the second sacrificial layer comprises copper. Other materials that can be etched selective to other relay materials are also useful. The copper is deposited by, for example, physical vapor deposition or sputtering. Other deposition techniques, depending on the sacrificial material, are also useful. Although not necessary, the first and second sacrificial layers comprise the same material.

The second sacrificial layer defines the gap between the first and second electrodes. In one embodiment, the thickness of the second sacrificial layer is about 0.5 μm to produce a 0.5 μm separation between the electrodes. The second sacrificial layer is patterned by conventional mask and etch techniques to define the area of the cavity.

Referring to Figs. 7, a dielectric layer 745 is deposited over the substrate to insulate the first electrode from the second electrode when the support member is moved upward. The thickness of the dielectric layer should be sufficient to provide electrical insulation as specified by design parameters. Typically the thickness of the dielectric layer is about 1 μm thick. Of course other thickness values can be used depending on design specifications and dielectric characteristics of the material.

The dielectric layer should provide good step coverage over the substrate topography created by the different features thereon. Silicon oxide or other dielectric layers such as silicon nitride can be used to form the dielectric layer.

In one embodiment, the dielectric layer comprises a silicon-rich silicon nitride (Si_3N_4) dielectric layer. The Si_3N_4 is deposited by PECVD. Other deposition techniques, such as LPCVD, are also useful. The dielectric layer is patterned, forming a contact opening to the first contact. Conventional masking and etching processes remove portions of the dielectric material to expose the contact opening 640.

The second electrode and second contact of the relay are formed. If necessary, a barrier layer can be provided on the surface of the second sacrificial layer prior to the formation of the contact and electrode to prevent

5 interdiffusion between the contact and sacrificial materials. Preferably, the barrier layer comprises a material that can be selectively removed to other relay materials. The barrier layer, for example, comprises titanium, chromium, tungsten, or palladium. Other barrier

10 materials which prevent interdiffusion between the contact and sacrificial materials are also useful.

In one embodiment, the electrode and contact are formed by electroplating techniques as previously described. A seed layer to promote plating of the

15 conductive material used to form the contact and electrode is formed by, for example, sputtering or other techniques on the substrate surface, covering the dielectric layer. In one embodiment, the seed layer comprises gold. Other materials that promote plating of the contact and electrode

20 materials are also useful.

A conductive material is electroplated on the seed layer. The conductive material serves as the electrode and contact. In one embodiment, the conductive material comprises gold or a gold alloy such as gold-palladium, gold

09890575-024102

An adhesion layer is provided to promote adhesion of the contact and electrode to the upper portion of the relay body. The adhesion layer comprises, in one embodiment, titanium. An adhesion layer comprising chromium or a material that promotes adhesion of the conductive material to the upper portion of the relay is also useful. The adhesion layer can be deposited by sputtering, evaporation, plating or other techniques. The seed layer, conductive layer, and adhesion layers are patterned using conventional mask and etch processes to form the second electrode 740 and second contact 730. The mask and etch processes also form leadouts and pads.

In an alternative embodiment, the conductive layer is deposited by sputtering, physical vapor deposition, or other deposition processes which do not require the use of a seed layer.

In yet another alternative embodiment, a mask layer is used to selectively plate the conductive material on the

seed layer to form the second electrode and second contact. The mask is removed after the electrode and contact are formed. An adhesion layer is deposited and patterned to form the adhesion layer over the second contact and second
5 electrode.

Referring to Fig. 8, a cap 805 is formed. The cap forms the upper portion of the relay, supporting the second electrode and second contact. The cap comprises a material sufficient to support the contact and electrode. In one
10 embodiment, the cap comprises nickel or a nickel alloy such as NiFe. Materials such as gold, plastic, epoxy, or others materials that can adequately support the relay components as needed can be used.

If a conductive material is used to form the cap, a
15 dielectric layer 880 is formed over the surface of the substrate prior the formation of the cap. The dielectric layer serves to insulate the cap from the contacts and electrodes to prevent shorting of the relay components. Various dielectrics can be used to form the dielectric
20 layer. In one embodiment, the dielectric comprises SiO₂ formed by PECVD. Other techniques to form the dielectric layer are also useful. The thickness of dielectric layer is sufficient to satisfy design specifications. The dielectric layer is, for example, about 1 μm thick. Of

2017-05-22 09:55:50

course, the thickness can be varied depending on the design specification and the dielectric material used.

The dielectric layer is patterned using conventional techniques to cover the region of the substrate on which the cap is formed. The pads to the contacts and electrodes are exposed to allow access thereto.

In one embodiment, the nickel cap is formed by electroplating. An adhesion layer is formed over the substrate to promote adhesion between the cap materials and the substrate. The adhesion layer comprises, for example, titanium. Other materials such as chromium or materials that promote adhesion are also useful. The adhesion layer is about 25 nm. A seed layer is formed over the substrate to facilitate plating of the cap material. The seed layer comprises, for example, nickel of about 50 μm thick. The nickel cap layer is then electroplated onto the seed layer. The thickness of the cap is, for example, about 5-10 μm . The cap layer along with the adhesion and seed layers are patterned to form the cap 805. The pads to the contacts and electrodes are exposed to allow access thereto.

Alternatively, a mask layer is formed to selectively plate the cap layer in the cap region. The mask is

0390575-01100
2007-05-24

removed, exposing the seed and adhesion layer. The exposed seed and adhesion layers are removed.

The backside of the substrate is masked, exposing the region of the substrate to be removed to form the

5 cantilever support member. The substrate is etched using ECE techniques. The etch removes the p-doped regions of the substrate, including the area surrounded by n-doped regions. The ECE stops or passivates on the n-doped regions and the nitride. A etch then removes the nitride

10 layer, exposing the sacrificial layers. The etch, which for example comprises a dry etch, employs chemistry that etches nitride selective to silicon. A wet etch is then employed to etch the sacrificial layers and barrier layers, separating the support member from the upper portion of the

15 relay body.

In another embodiment, the first contact is not provided with a lead out. Instead, the first contact serves as a bridging contact to electrically couple first and second contacts located in the second contact region in

20 the upper portion of the relay body.

Referring to Fig. 9, a substrate 301 having been processed to include a patterned first sacrificial layer 930 is shown. First and second contact openings 940 and 941 are formed, exposing the surface of the first contact

09890575-03403
20171205 14:50:55

beneath. The process continues, providing a second sacrificial layer and dielectric layer as previously described.

Referring to Fig. 10, electrode 140 and second and
5 third contacts 150 and 151 are formed. Contact pads and leadouts can also be provided for the electrode and the contacts. The contacts and electrode are formed by electroplating techniques. Other techniques, such as depositing a conducting layer and patterning it to form the
10 contacts and electrodes, are also useful. The process continues as previously described to complete the relay.

In an alternative embodiment of the invention, the support member is defined by removing portions of the substrate. Referring to Fig. 11, a substrate 101 is
15 provided. The substrate is, for example, a silicon wafer. Other types of substrates are also useful. The substrate comprises a first doped region, such as a p-doped region. The first doped region can be either part of the substrate or formed. A second doped region 107, such as an n-doped
20 region, is provided on the surface of the substrate. The depth of the second doped region defines the thickness of the support member.

As the support member is defined by etching into the surface of the substrate, it is not necessary to provide a

093057E-02403

heavily doped region. An etch mask is formed on the surface of the substrate and patterned to expose portions of the substrate. The pattern of the mask defines the shape of the support member.

5 The exposed portions of the substrate are removed by an anisotropic etch such as RIE, forming a trench 180 on the surface of the substrate. The depth of the trench is deeper than the n-doped region to ensure correct formation of the support member by the subsequent ECE etch as
10 described in Fig. 8.

A barrier layer 190 for the subsequent ECE etch as described in Fig. 8 is formed on the substrate, covering the surface and lining the trench. In one embodiment, the ECE etch barrier comprises Si_3N_4 . Other materials that is
15 not etched by the ECE etch chemistry can also be used.

Referring to Fig. 12, a plug 196 is provided over the trench 180 to enable subsequent processing. The plug comprises, in one embodiment, aluminum. The plug is formed by sputtering aluminum on the substrate. Other deposition
20 techniques are also useful.

Alternatively, the plug material comprises silicate glass. In one embodiment, the plug material comprises doped silicate glass. The doped silicate is, for example borophosphosilicate glass (BPSG). Materials that can fill

the trenches are also useful. Other doped silicate glass, such as phosphosilicate glass (PSG) and borosilicate glass (BSG), or undoped silicate glass can also be used. If the plug material used is unaffected by the etch ECE chemistry, a separate ECE barrier layer is not necessary since the plug can serve as the ECE barrier.

The process for forming the relay continues as described in Fig. 4.

In another embodiment, a dual support member is provided in the relay as described in Fig. 2. Fabricating the dual support member relay is similar to the process already described except a second support member is defined during the process of defining the support member.

To form the over-travel region, an additional sacrificial layer is used. In one embodiment, the dual support member relay employs the use of three sacrificial layers. For example, the first sacrificial layer defines the gap between the first and second contacts, the second sacrificial layer defines the over-travel region, and the third sacrificial layer defines the gap between the first and second electrodes.

While the invention has been particularly shown and described with reference to various embodiments, it will be

recognized by those skilled in the art that modifications and changes may be made to the present invention without departing from its scope. The scope of the invention should therefore be determined not with reference to the

5 above description but with reference to the appended claims along with their full scope of equivalents.

09890575.024103

What is claimed is:

1. A microstructure relay comprising:

a body including upper and lower portions, wherein the
5 lower portion is formed from a substrate and the upper
portion is formed on the substrate to avoid bonding of the
lower portion to the upper portion;

a support member having a first end fixed to the body
to form a cantilever, wherein an upper surface of the
10 support member and a lower surface of the upper portion of
the body forms a cavity; and

a first contact region located on the upper surface at
a second end of the support member, the first contact
region comprising a first contact, wherein pivoting the
15 support member toward the lower surface causes the first
contact to be electrically coupled to a counter contact.

2. The microstructure relay of claim 1 wherein the
support member is pivoted toward the lower surface by
20 electrostatic force, the electrostatic force generated by
applying a voltage potential to the first and second
electrodes, the first electrode is located on the upper
surface and the second electrode is located on the lower
surface.

09890575 034103
201120 52505850

5

10

15

20

7. The microstructure relay of claim 6 further comprising an over-travel region in the lower surface, the over-travel

8. The microstructure relay of claim 7 wherein the
5 support member comprises silicon.

10 10. The microstructure relay of claim 9 wherein the second stress layer comprises silicon nitride.

15

13. The micro-structure relay of claim 12 further
20 comprising a dielectric layer insulating the upper portion
of the body from the second contact and second electrode.

14. The microstructure relay of claim 7 wherein the support member comprises nickel.

15. The microstructure relay of claim 14 wherein the first stress-inducing layer comprises silicon oxide and the second stress-inducing layer comprises polysilicon.

5

16. The microstructure relay of claim 15 wherein the polysilicon comprises doped polysilicon.

17. The micro-structure relay of claim 16 further
10 comprises a compensation layer on a surface of the support member opposite the upper surface, the compensation layer having a thermal coefficient of expansion (TCE) similar in magnitude to a TCE of the first stress-inducing layer.

15 18. The micro-structure relay of claim 17 wherein the intrinsic stress of the compensation layer lower than the intrinsic stress of the first stress layer to reduce the influence of the compensation layer on the support member.

20 19. The microstructure relay of claim 2 further comprises a second support member, the second support member having a first end fixed to the body and the second contact supported at a second end on an upper surface of the second support member.

09890575 03440
201709 52505850

20. The micro-structure relay of claim 19 further comprises a stress-inducing layer on the upper surfaces of the first and second support members, the stress layer inducing a compressive stress on support member to cause it to bend away from the lower surface upper portion of the body.

21. The microstructure relay of claim 20 wherein the support member comprises silicon.

22. The microstructure relay of claim 21 wherein the stress-inducing layer comprises silicon oxide.

23. The microstructure relay of claim 22 wherein the second support member is shorter than the first support member.

24. The microstructure relay of claim 23 wherein an over-travel is defined by the second support member.

25. The microstructure relay of claim 24 further comprises an over-travel region on the second support member to

09890575.024400
2017.02.05 15:25:06

ensure the over-travel is not obstructed as the first support member is pivoted toward the lower surface.

26. A micro-relay comprising:

- 5 a body including upper and lower portions,
 a support member supported at a first end by the body
to form a cantilever, wherein a major surface of the
support member and a lower surface of the upper portion of
the body forms a cavity; and
- 10 a first contact region located on the major surface at
a second end of the support member, the first contact
region comprising a first contact,
 the support member comprising an s-shape, wherein a
body of the support member bends in a direction away from
15 the surface of the upper portion of the body and the first
contact region bends in a direction toward the surface of
the upper portion of the body,
 the s-shaped support member, when pivoted toward the
lower surface, causes the first contact to be electrically
20 coupled to a counter contact.

27. The micro-structure relay of claim 26 wherein the support member is pivoted toward the lower surface by electrostatic force, the electrostatic force generated by

applying a voltage potential to the first and second electrodes, the first electrode is located on the upper surface and the second electrode is located on the lower surface.

5

28. The microstructure relay of claim 27 further comprises a second contact region on the lower surface, the second contact region comprising the second contact.

10 29. The micro-structure relay of claim 28 wherein the s-shape support member comprises first and second stress layers, the first stress layer inducing a compressive stress on the support member to cause it to bend away from the lower surface, and the second stress layer inducing a
15 tensile stress on the first contact region to cause the first contact region to bend toward the lower surface.

30. The microstructure relay of claim 29 wherein the bend of the first contact region toward the lower surface
20 defines an over-travel.

31. The microstructure relay of claim 30 further comprising an over-travel region in the lower surface, the over-travel region accommodating the bend of the first

2014052505360

contact region to prevent the over-travel from being obstructed.

32. The microstructure relay of claim 31 wherein the
5 support member comprises silicon.

33. The microstructure relay of claim 32 wherein the first stress layer comprises silicon oxide.

10 34. The microstructure relay of claim 33 wherein the second stress layer comprises silicon nitride.

35. The microstructure relay of claim 34 wherein the lower portion of the body comprises silicon.

15

36. The microstructure relay of claim 35 wherein the upper portion comprises nickel.

37. The micro-structure relay of claim 36 further
20 comprising a dielectric layer insulating the upper portion of the body from the second contact and second electrode.

38. The microstructure relay of claim 31 wherein the support member comprises nickel.

099065 0440
201209260660

39. The microstructure relay of claim 38 wherein the first stress-inducing layer comprises silicon oxide and the second stress-inducing layer comprises polysilicon.

5

40. The microstructure relay of claim 39 wherein the polysilicon comprises doped polysilicon.

41. The micro-structure relay of claim 40 further
10 comprises a compensation layer on a surface of the support member opposite the upper surface, the compensation layer having a thermal coefficient of expansion (TCE) similar in magnitude to a TCE of the first stress-inducing layer.

15 42. The micro-structure relay of claim 41 wherein the intrinsic stress of the compensation layer lower than the intrinsic stress of the first stress layer to reduce the influence of the compensation layer on the support member.

20 43. The micro-structure relay of claim 27 further comprises a second support member, the second support member having a first end fixed to the body and the second contact supported at a second end on an upper surface of the second support member, wherein the support member is

2017.02.05 15:03:40

pivoted toward the lower surface by electrostatic force,
the electrostatic force generated by applying a voltage
potential to the first and second electrodes, the first
electrode is located on the upper surface and the second
5 electrode is located on the lower surface.

44. The micro-structure relay of claim 43 further
comprises a stress-inducing layer on the upper surfaces of
the first and second support members, the stress layer
10 inducing a compressive stress on support member to cause it
to bend away from the lower surface upper portion of the
body.

45. The microstructure relay of claim 44 wherein the
15 support member comprises silicon.

46. The microstructure relay of claim 45 wherein the
stress-inducing layer comprises silicon oxide.

20 47. The microstructure relay of claim 46 wherein the
second support member is shorter than the first support
member.

09890575-024400
2007-02-05 12:50:53

48. The microstructure relay of claim 47 wherein an over-travel is defined by the second support member.

49 The microstructure relay of claim 48 further comprises
5 an over-travel region on the second support member to ensure the over-travel is not obstructed as the first support member is pivoted toward the lower surface.

50. A process for fabricating microstructures comprising:
10 providing a substrate with a first doped region comprising a dopants of a first type;

providing a second doped region comprising dopants of a second type, wherein the first and second doped regions form a pattern on a surface of the substrate corresponding
15 to a feature;

etching process to remove the first or the second doped region from the surface of the substrate to form the feature, wherein the doped region that is removed comprises a high dopant concentration to enable the etchant to form
20 the openings with lateral dimensions of less than 250 μm .

51. The process as recited in claim 50 wherein the high dopant concentration in the doped region that is removed

20110519 10:41:03

52. The process as recited in claim 50 wherein the high
5 dopant concentration in the doped region that is removed
comprises a dopant concentration that results in a
resistivity of about 50 mohm - cm.

54. The process as recited in claim 50 wherein the first region comprises p-type dopants to form a p-type dopant region and the second doped region comprises n-type dopants to form an n-doped region.

56. The process as recited in claim 54 wherein the p-typed doped region is provided by implanting p-type dopants into

the substrate and the n-typed doped region is provided by selectively implanting n-type dopants into the substrate to form the pattern.

5 57. The process as recited in claims 55 or 56 wherein the p-type region is removed by electrochemical etching.

58. The process as recited in claim 57 wherein the dopant concentration of the p-type doped region results in the p-
10 typed region having a resistivity less than 6 ohms - cm.

59. The process as recited in claim 57 wherein the dopant concentration of the p-type doped region results in the p-typed region having a resistivity of about 50 mohm - cm.

15

60. The process as recited in claim 59 wherein the opening is less than 60 μm .

207120"54506860

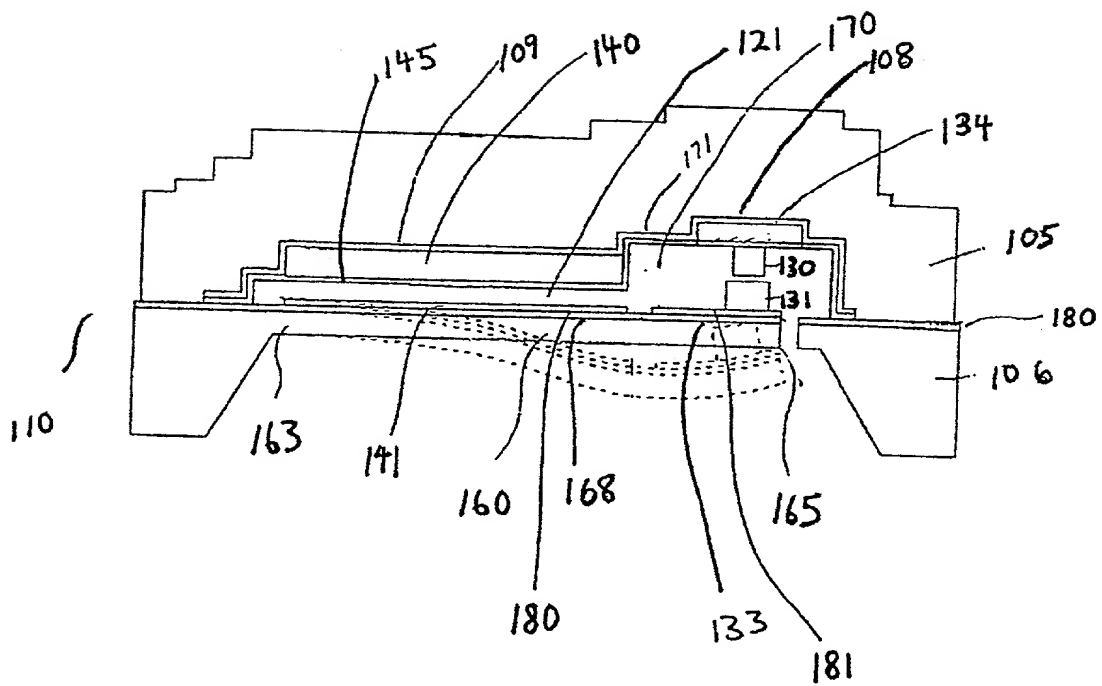


FIG.1A

2/14

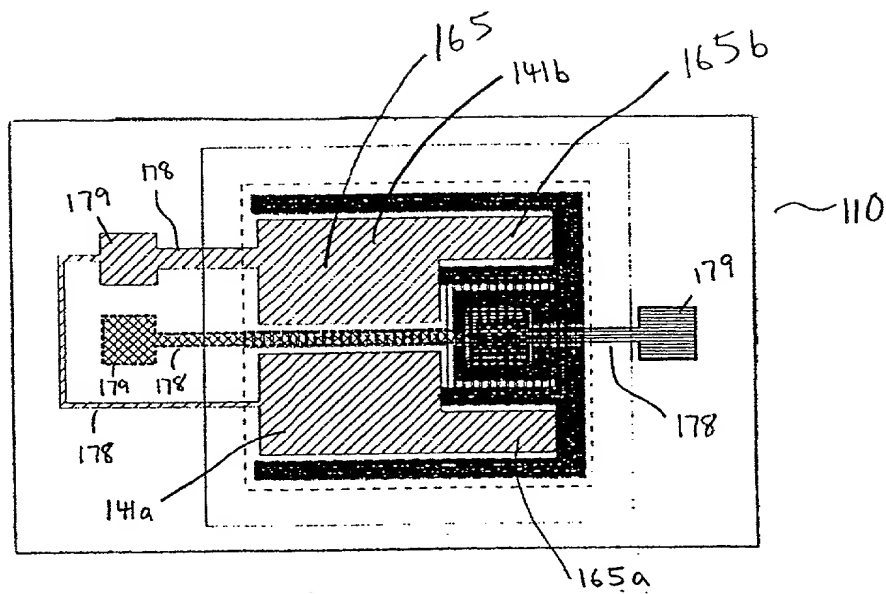
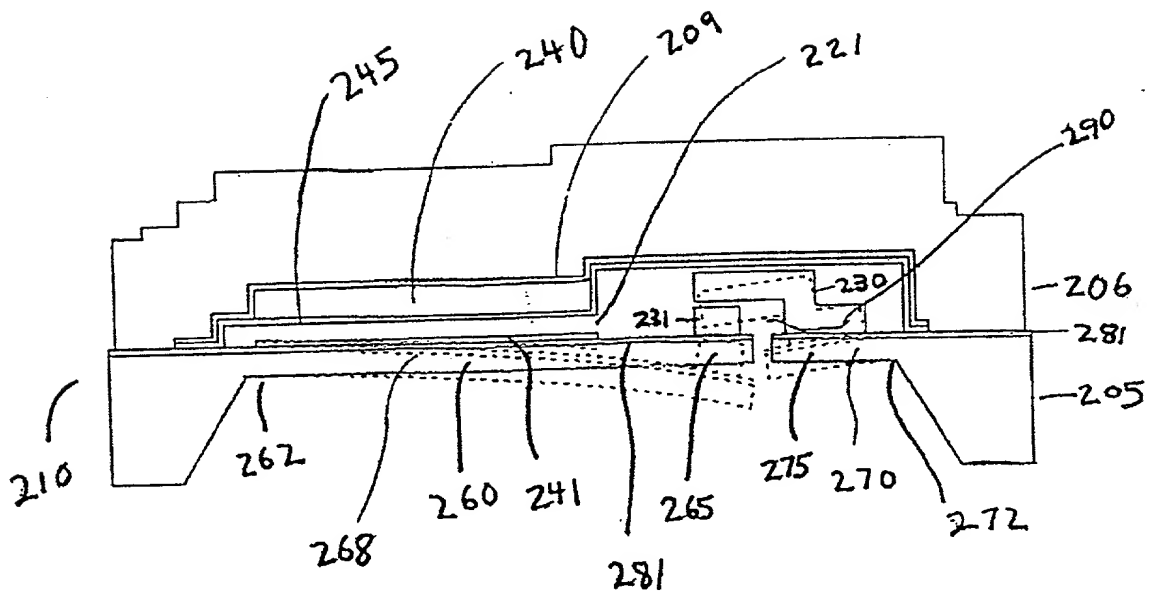


FIG.1B

201420 92506860

3/14



4/14

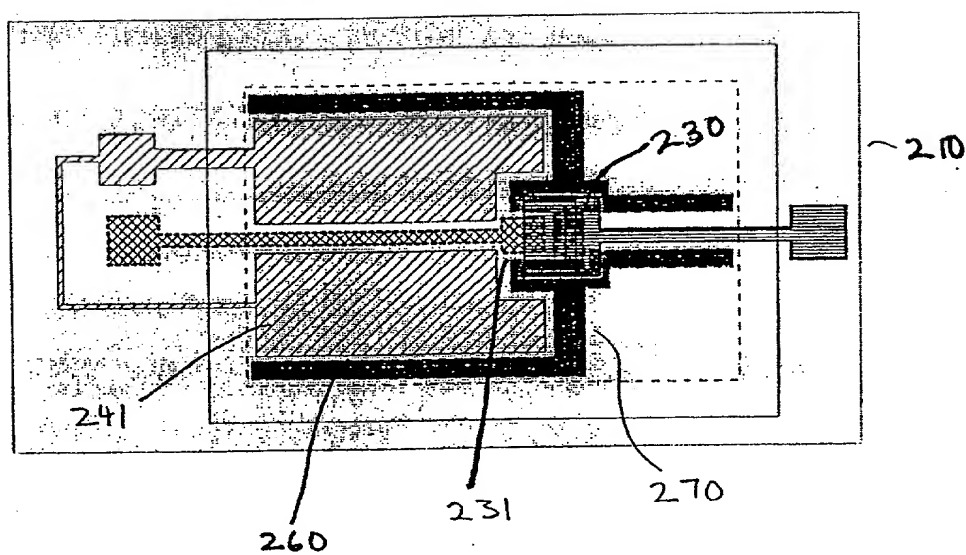


FIG.2B

5/14

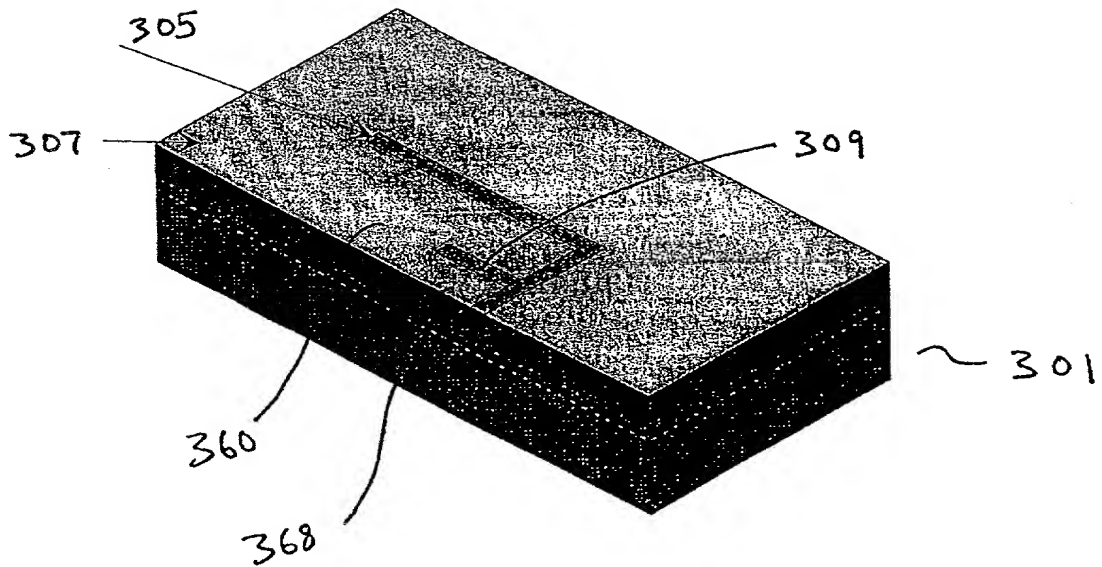


FIG.3

6/14

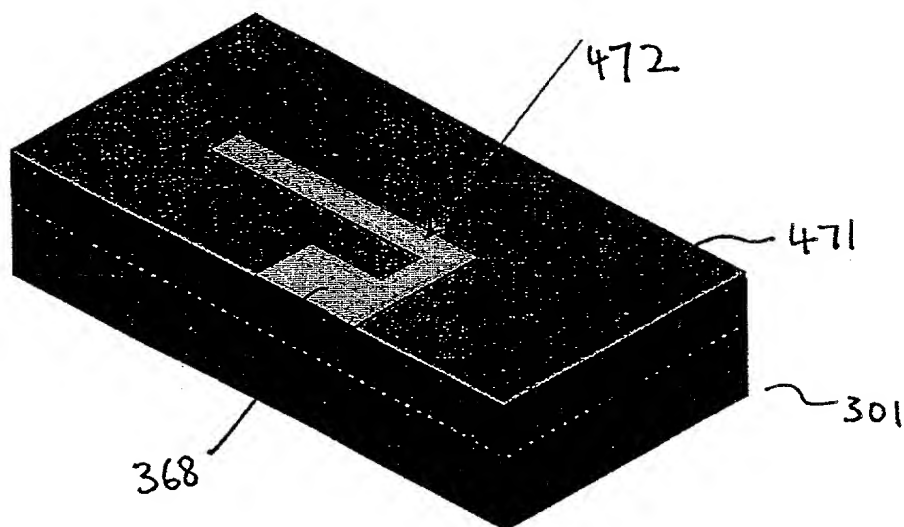


FIG. 4

7/14

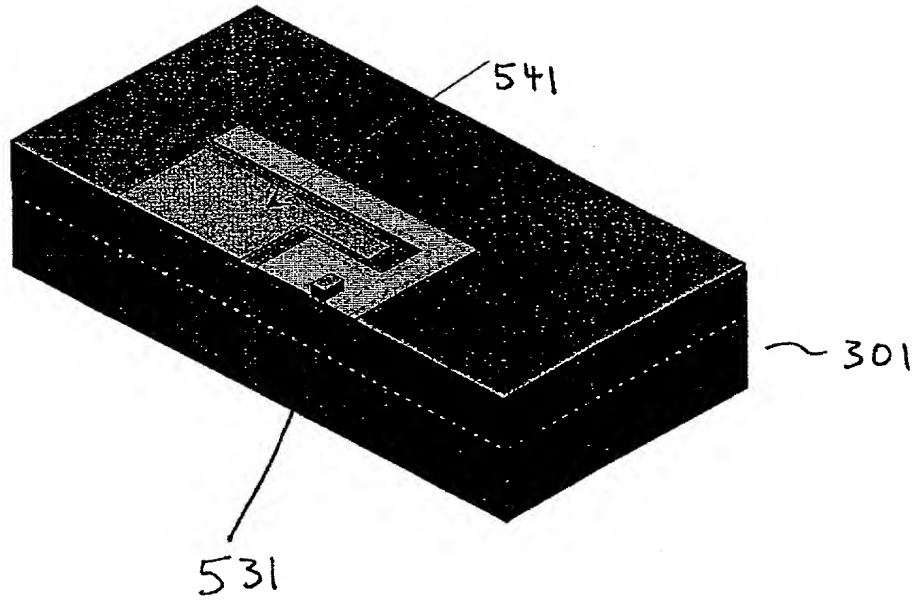


FIG.5

09890575 02440

8/14

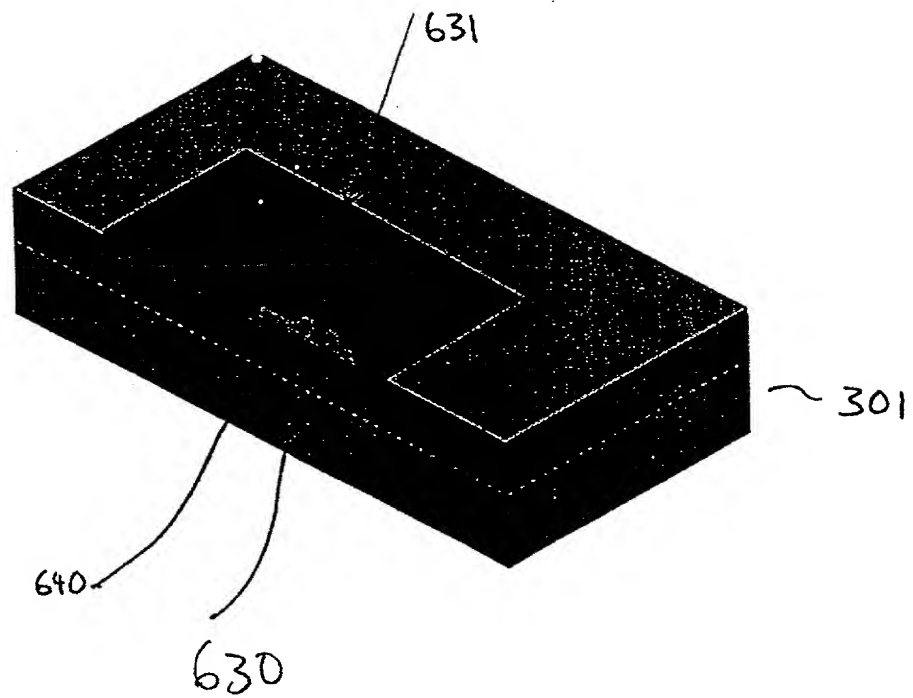


FIG. 6

9/14

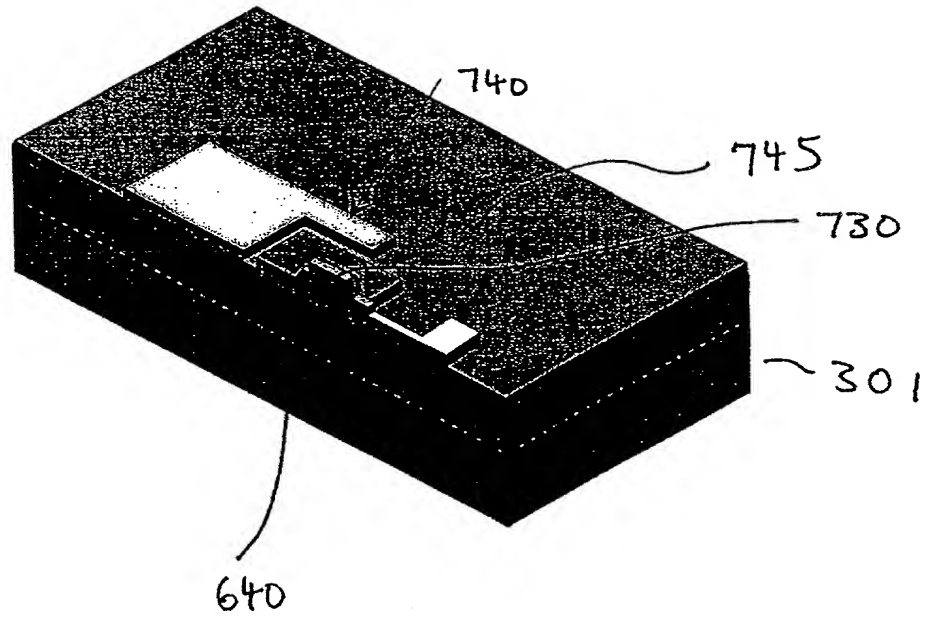


FIG. 7

09890575 9/14

10/14

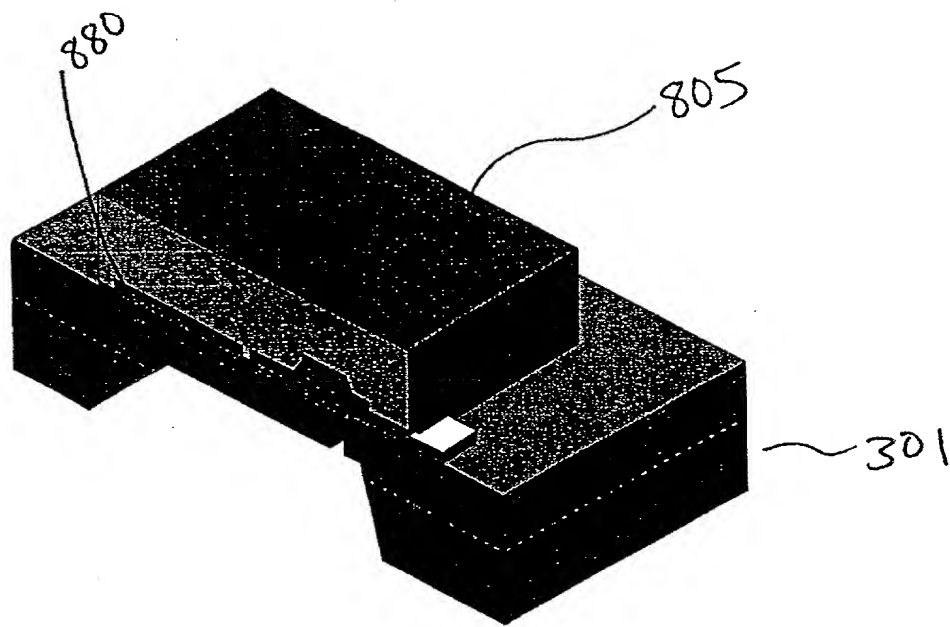


FIG. 8

11/14

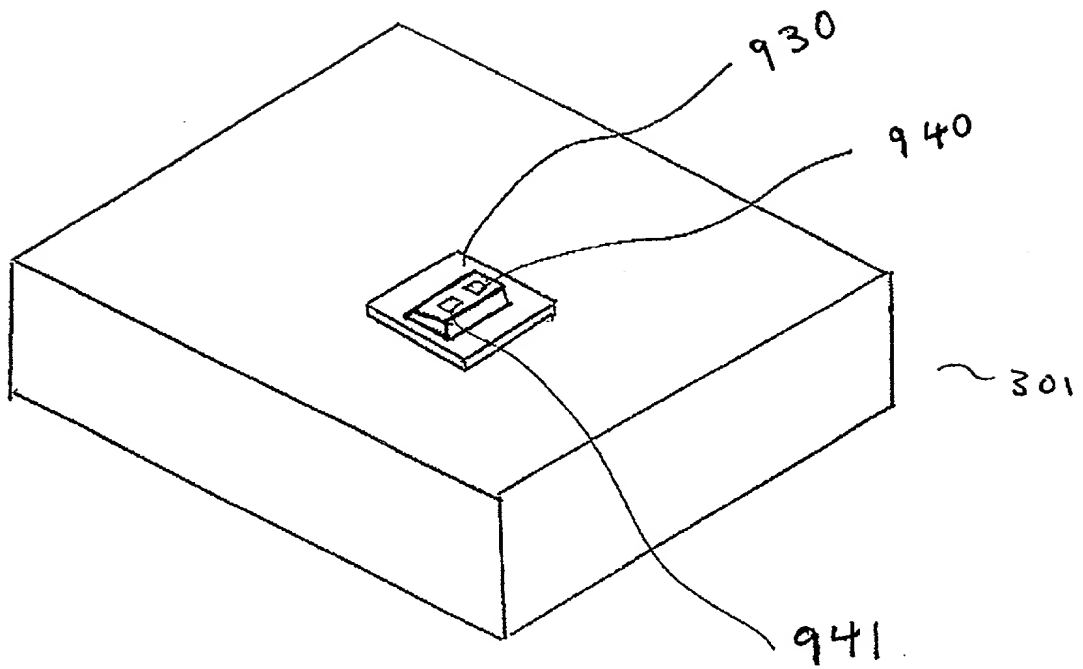


FIG.9

12/14

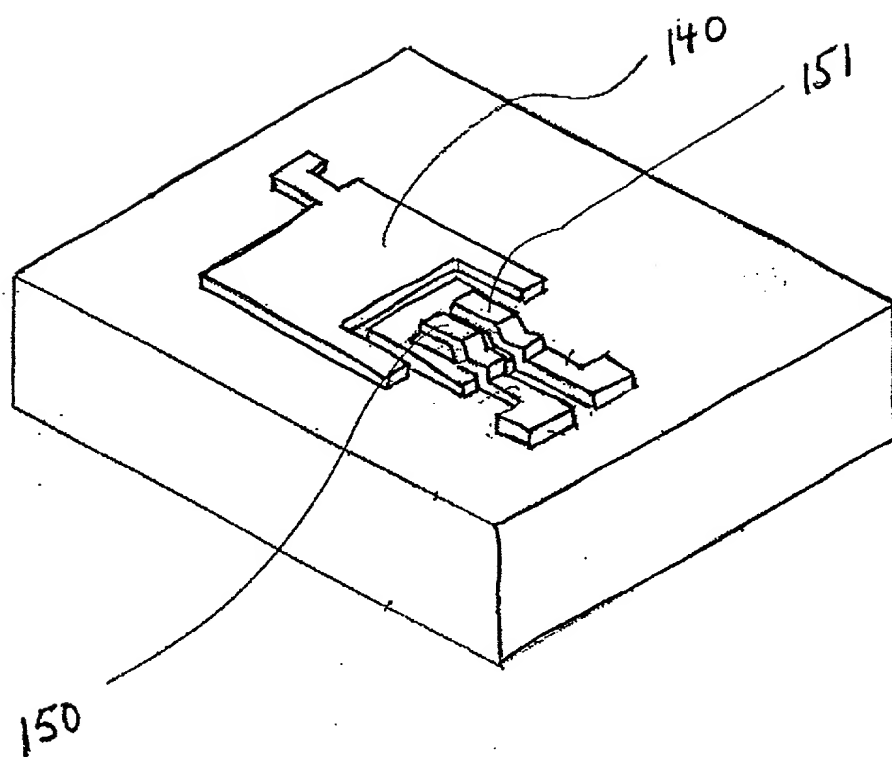


FIG.10

13/14

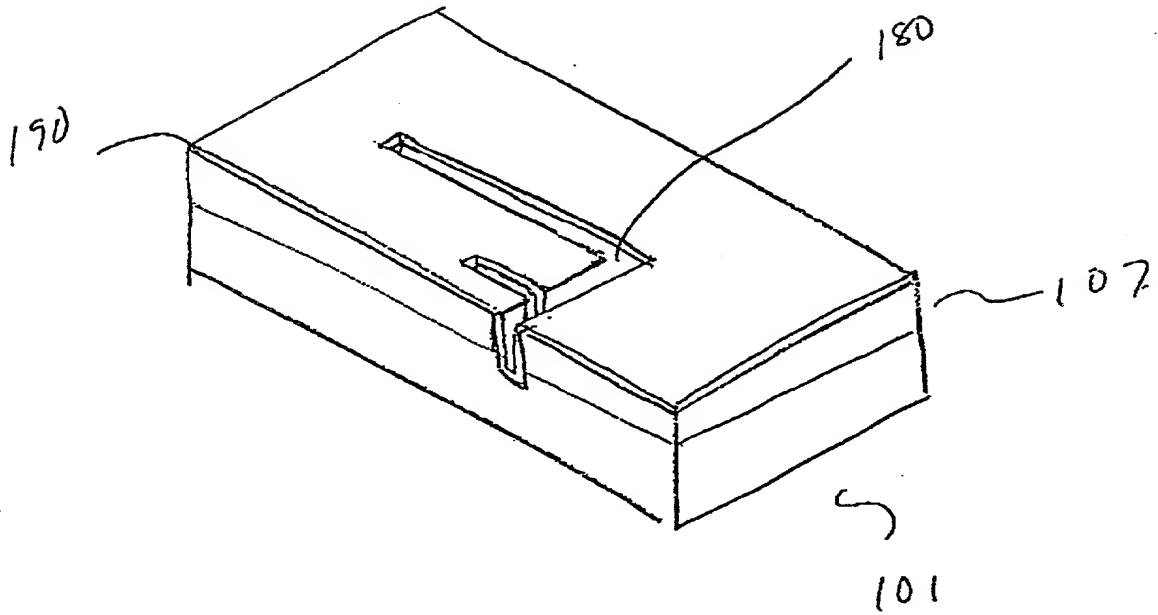


FIG.11

14/14

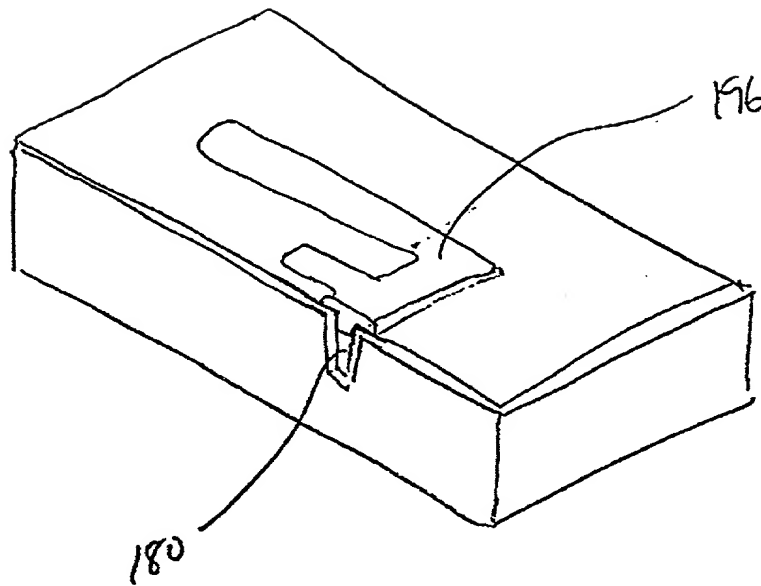


FIG.12

Declaration and Power of Attorney for Patent Application

Erklärung für Patentanmeldungen mit Vollmacht

German Language Declaration

Als nachstehend benannter Erfinder erkläre ich hiermit an Eides Statt:

Dass mein Wohnsitz, meine Postanschrift und meine Staatsangehörigkeit den im nachstehenden nach meinem Namen aufgeführten Angaben entsprechen;

dass ich nach bestem Wissen der ursprüngliche, erste und alleinige Erfinder (falls nachstehend nur ein Name angegeben ist) oder ein ursprünglicher, erster und Miterfinder (falls nachstehend mehrere Namen aufgeführt sind) des Gegenstandes bin, für den dieser Antrag gestellt wird und für den ein Patent beantragt wird für die Erfindung mit dem Titel:

deren Beschreibung

(Zutreffendes ankreuzen)

☐ hier beigefügt ist.

☐ am _____ unter der

Anmeldungsseriennummer _____

eingereicht wurde und am _____

abgeändert wurde (falls tatsächlich abgeändert).

Ich bestätige hiermit, dass ich den Inhalt der obigen Patentanmeldung, einschliesslich der Ansprüche, durchgesehen und verstanden habe, die eventuell durch einen Zusatzantrag, wie oben erwähnt, abgeändert wurde.

Ich erkenne meine Pflicht zur Offenbarung irgendwelcher Informationen, die für die Prüfung der vorliegenden Anmeldung in Einklang mit Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) von Wichtigkeit sind, an.

Ich beanspruche hiermit ausländische Prioritätsvorteile gemäss Abschnitt 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 119 aller unten angegebenen Auslandsanmeldungen für ein Patent oder eine Erfinderurkunde, und habe auch alle Auslandsanmeldungen für ein Patent oder eine Erfinderurkunde nachstehend gekennzeichnet, die ein Anmeldedatum haben, das vor dem Anmeldedatum der Anmeldung liegt, für die Priorität beansprucht wird.

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

MICRO-RELAY

the specification of which

(check one)

☐ is attached hereto.

☒ was filed on **PCT Filing Date: February 4, 1999**

Application Serial No. **09/890,575**

and was amended on _____

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

German Language Declaration

Prior foreign applications

Priorität beansprucht

Priority Claimed

(Number) (Country) Day/Month/Year Filed
(Nummer) (Land) Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number) (Country) Day/Month/Year Filed
(Nummer) (Land) Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number) (Country) Day/Month/Year Filed
(Nummer) (Land) Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

Ich beanspruche hiermit gemäss Absatz 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 120, den Vorzug aller unten aufgeführten Anmeldungen, und falls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in einer früheren amerikanischen Patentanmeldung laut dem ersten Paragraphen des Absatzes 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 112, offenbart ist, erkenne ich gemäss Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung und dem nationalen oder PCT internationalen Anmeldedatum dieser Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

PCT/SG99/00005

(Application Serial No.)
(Anmeldeseriennummer)

February 4, 1999

(Filing Date)
(Anmeldedatum)

pending

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

(Application Serial No.)
(Anmeldeseriennummer)

(Filing Date)
(Anmeldedatum)

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

Ich erkläre hiermit, dass alle von mir in der vorliegenden Erklärung gemachten Angaben nach meinem besten Wissen und Gewissen der vollen Wahrheit entsprechen, und dass ich diese eidesstattliche Erklärung in Kenntnis dessen abgebe, dass wissentlich und vorsätzlich falsche Angaben gemäss Paragraph 1001, Absatz 18 der Zivilprozessordnung der Vereinigten Staaten von Amerika mit Geldstrafe belegt und/oder Gefängnis bestraft werden können, und dass derartig wissentlich und vorsätzlich falsche Angaben die Gültigkeit der vorliegenden Patentanmeldung oder eines darauf erteilten Patentbeschlusses gefährden können.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

German Language Declaration

VERTRETUNGSVOLLMACHT: Als benannter Erfinder beauftrage ich hiermit den nachstehend benannten Patentanwalt (oder die nachstehend benannten Patentanwälte) und/oder Patent-Agenten mit der Verfolgung der vorliegenden Patentanmeldung sowie mit der Abwicklung aller damit verbundenen Geschäfte vor dem Patent- und Warenzeichenamt: (Name und Registrationsnummer anführen)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

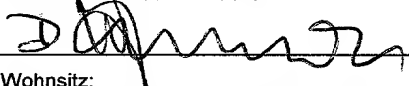
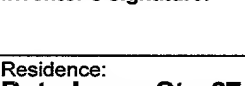
Salvatore Anastasi (Registration No. 39090),
Michael J. Aronoff (Registration No. 37770),
Stephen J. Driscoll (Registration No. 37564),
Robert J. Kapalka (Registration No. 34198),
Driscoll A. Nina (Registration No. 34685),
Joseph A. Tessari (Registration No. 32177), and
Bruce J. Wolstoncroft (Registration No. 32075).
Direct Telephone Calls to: (name and telephone number)
Robert J. Kapalka (302) 633 2771

Telefongespräche bitte richten an:
(Name und Telefonnummer)

Postanschrift:

Send Correspondence to:

Tyco Technology Resources
4550 New Linden Hill Road
Suite 450
Wilmington, DE 19808

Voller Name des einzigen oder ursprünglichen Erfinders: Dirk WAGENAAR 27.11.01		Full name of third joint inventor, if any: Helmut SCHLAAK	
Unterschrift des Erfinders: 		Inventor's signature: 	
Datum: 27.11.01		Date:	
Wohnsitz: 103 Farrer Drive, #01-01, Singapore 259 295 SGX		Residence: Potsdamer Str. 67, 64372 Ober-Ramstadt, Germany	
Staatsangehörigkeit: German		Citizenship: German	
Postanschrift: 103 Farrer Drive, #01-01, Singapore 259 295		Post Office Address: Potsdamer Str. 67, 64372 Ober-Ramstadt,	
		Germany	
Voller Name des zweiten Miterfinders, falls zutreffend: Kay KRUPKA		Full name of fourth joint inventor, if any: Uppili SRIDHAR	
Unterschrift des Erfinders:		Inventor's signature:	
Datum:		Date:	
Wohnsitz: Wolkenburgweg 2, 14169 Berlin, Germany		Residence: 8 Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210	
Staatsangehörigkeit: German		Citizenship: US	
Postanschrift: Wolkenburgweg 2, 14169 Berlin, Germany		Post Office Address: Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210	

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterfindern angeben).

(Supply similar information and signature for third and subsequent joint inventors.)

2

Declaration and Power of Attorney for Patent Application

Erklärung für Patentanmeldungen mit Vollmacht

German Language Declaration

Als nachstehend benannter Erfinder erkläre ich hiermit, an
Eides Statt:

As a below named inventor, I hereby declare that:

Dass mein Wohnsitz, meine Postanschrift und meine
Staatsangehörigkeit den im nachstehenden nach meinem
Namen aufgeführten Angaben entsprechen:

My residence, post office address and citizenship are as
stated below next to my name,

dass ich nach bestem Wissen der ursprüngliche, erste und
alleinige Erfinder (falls nachstehend nur ein Name angege-
ben ist) oder ein ursprünglicher, erster und Miterfinder (falls
nachstehend mehrere Namen aufgeführt sind) des Gegen-
standes bin, für den dieser Antrag gestellt wird und für den
ein Patent beantragt wird für die Erfindung mit dem Titel:

I believe I am the original, first and sole inventor (if only one
name is listed below) or an original, first and joint inventor (if
plural names are listed below) of the subject matter which is
claimed and for which a patent is sought on the invention
entitled:

MICRO-RELAY

deren Beschreibung

(Zutreffendes ankreuzen)

☐

hier beigefügt ist.

☐

am _____ unter der

Anmeldungsseriennummer _____

eingereicht wurde und am _____
abgeändert wurde (falls tatsächlich abgeändert).

Ich bestätige hiermit, dass ich den Inhalt der obigen Patent-
anmeldung, einschliesslich der Ansprüche, durchgesehen
und verstanden habe, die eventuell durch einen Zusatz-
antrag, wie oben erwähnt, abgeändert wurde.

Ich erkenne meine Pflicht zur Offenbarung irgendwelcher In-
formationen, die für die Prüfung der vorliegenden
Anmeldung in Einklang mit Absatz 37, Bundesgesetzbuch,
Paragraph 1.56(a) von Wichtigkeit sind, an.

Ich beanspruche hiermit ausländische Prioritätsvorteile ge-
mass Abschnitt 35 der Zivilprozessordnung der Vereinigten
Staaten, Paragraph 119 aller unten angegebenen Auslands-
anmeldungen für ein Patent oder eine Erfinderurkunde, und
habe auch alle Auslandsanmeldungen für ein Patent oder
eine Erfinderurkunde nachstehend gekennzeichnet, die ein
Anmeldedatum haben, das vor dem Anmeldedatum der
Anmeldung liegt, für die Priorität beansprucht wird.

the specification of which

(check one)

☐

is attached hereto.

☒

was filed on **PCT Filing Date: February 4, 1999**

Application Serial No. **09/890,575**

and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the con-
tents of the above identified specification, including the
claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is ma-
terial to the examination of this application in accordance
with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United
States Code, § 119 of any foreign application(s) for patent
or inventor's certificate listed below and have also identified
below any foreign application for patent or inventor's certi-
ficate having a filing date before that of the application on
which priority is claimed:

German Language Declaration

Prior foreign applications
Priorität beansprucht

Priority Claimed

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

Ich beanspruche hiermit gemäss Absatz 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 120, den Vorzug aller unten aufgeführten Anmeldungen, und falls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in einer früheren amerikanischen Patentanmeldung laut dem ersten Paragraphen des Absatzes 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 112, offenbart ist, erkenne ich gemäss Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung und dem nationalen oder PCT internationalen Anmeldedatum dieser Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

PCT/SG99/00005

(Application Serial No.)
(Anmeldeseriennummer)

February 4, 1999

(Filing Date)
(Anmeldedatum)

pending

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

(Application Serial No.)
(Anmeldeseriennummer)

(Filing Date)
(Anmeldedatum)

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

Ich erkläre hiermit, dass alle von mir in der vorliegenden Erklärung gemachten Angaben nach meinem besten Wissen und Gewissen der vollen Wahrheit entsprechen, und dass ich diese eidesstattliche Erklärung in Kenntnis dessen abgebe, dass wissentlich und vorsätzlich falsche Angaben gemäss Paragraph 1001, Absatz 18 der Zivilprozessordnung der Vereinigten Staaten von Amerika mit Geldstrafe belegt und/oder Gefängnis bestraft werden können, und dass derartig wissentlich und vorsätzlich falsche Angaben die Gültigkeit der vorliegenden Patentanmeldung oder eines darauf erteilten Patentes gefährden können.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

German Language Declaration

VERTRETUNGSVOLLMACHT: Als benannter Erfinder beauftrage ich hiermit den nachstehend benannten Patentanwalt (oder die nachstehend benannten Patentanwälte) und/oder Patent-Agenten mit der Verfolgung der vorliegenden Patentanmeldung sowie mit der Abwicklung aller damit verbundenen Geschäfte vor dem Patent-und Warenzeichenamt: (Name und Registrationsnummer anführen)

Telefongespräche bitte richten an:
(Name und Telefonnummer)

Postanschrift:

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

Salvatore Anastasi (Registration No. 39090),
Michael J. Aronoff (Registration No. 37770),
Stephen J. Driscoll (Registration No. 37564),
Robert J. Kapalka (Registration No. 34198),
Driscoll A. Nina (Registration No. 34685),
Joseph A. Tessari (Registration No. 32177), and
Bruce J. Wolstoncroft (Registration No. 32075).
Direct Telephone Calls to: (name and telephone number)
Robert J. Kapakla (302) 633 2771

Send Correspondence to:

Tyco Technology Resources
4550 New Linden Hill Road
Suite 450
Wilmington, DE 19808

Voller Name des einzigen oder ursprünglichen Erfinders: Dirk WAGENAAR	Full name of third joint inventor, if any: Helmut SCHLAAK
Unterschrift des Erfinders: _____ Datum: _____	Inventor's signature: _____ Date: _____
Wohnsitz: 103 Farrer Drive, #01-01, Singapore 259 295	Residence: Potsdamer Str. 67, 64372 Ober-Ramstadt, Germany
Staatsangehörigkeit: German	Citizenship: German
Postanschrift: 103 Farrer Drive, #01-01, Singapore 259 295	Post Office Address: Potsdamer Str. 67, 64372 Ober-Ramstadt, Germany
Voller Name des zweiten Miterfinders, falls zutreffend: Kay KRUPKA	Full name of fourth joint inventor, if any: Uppili SRIDHAR
Unterschrift des Erfinders: <i>K. Krupka</i> Datum: <i>01-11-13</i>	Inventor's signature: _____ Date: _____
Wohnsitz: Wolkenburgweg 2, 14169 Berlin, Germany DEX	Residence: 8 Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210
Staatsangehörigkeit: German	Citizenship: US
Postanschrift: Wolkenburgweg 2, 14169 Berlin, Germany	Post Office Address: 8 Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterfindern angeben).

(Supply similar information and signature for third and subsequent joint inventors.)

Voller Name des fünften Miterfinders, falls zutreffend:		Full name of seventh joint inventor, if any:	
		Victor D. SAMPER	
Unterschrift des Erfinders:	Datum:	Inventor's signature:	Date:
Wohnsitz:		Residence:	
		38 Dover Rise, Tower A, Dover Parkview #03-12, Singapore 138684	
Staatsangehörigkeit:		Citizenship:	
		British	
Postanschrift:		Post Office Address:	
		38 Dover Rise, Tower A, Dover Parkview #03-12, Singapore 138684	
Voller Name des sechsten Miterfinders, falls zutreffend:		Full name of eighth joint inventor, if any:	
		Pang Dow FOO	
Unterschrift des Erfinders:	Datum:	Inventor's signature:	Date:
Wohnsitz:		Residence:	
		360 Pasir Panjang Road, #03-05, Singapore 118699	
Staatsangehörigkeit:		Citizenship:	
		US	
Postanschrift:		Post Office Address:	
		360 Pasir Panjang Road, #03-05, Singapore 118699	

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterfindern angeben).

(Supply similar information and signature for third and subsequent joint inventors.)

3

Declaration and Power of Attorney for Patent Application

Erklärung für Patentanmeldungen mit Vollmacht

German Language Declaration

Als nachstehend benannter Erfinder erkläre ich hiermit an Eides Statt:

Dass mein Wohnsitz, meine Postanschrift und meine Staatsangehörigkeit den im nachstehenden nach meinem Namen aufgeführten Angaben entsprechen;

dass ich nach bestem Wissen der ursprüngliche, erste und alleinige Erfinder (falls nachstehend nur ein Name angegeben ist) oder ein ursprünglicher, erster und Miterfinder (falls nachstehend mehrere Namen aufgeführt sind) des Gegenstandes bin, für den dieser Antrag gestellt wird und für den ein Patent beantragt wird für die Erfindung mit dem Titel:

deren Beschreibung

(Zutreffendes ankreuzen)

☐ hier beigelegt ist.

☐ am _____ unter der

Anmeldungsseriennummer _____

eingereicht wurde und am _____

abgeändert wurde (falls tatsächlich abgeändert).

Ich bestätige hiermit, dass ich den Inhalt der obigen Patentanmeldung, einschliesslich der Ansprüche, durchgesehen und verstanden habe, die eventuell durch einen Zusatzantrag, wie oben erwähnt, abgeändert wurde.

Ich erkenne meine Pflicht zur Offenbarung irgendwelcher Informationen, die für die Prüfung der vorliegenden Anmeldung in Einklang mit Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) von Wichtigkeit sind, an.

Ich beanspruche hiermit ausländische Prioritätsvorteile gemäss Abschnitt 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 119 aller unten angegebenen Auslandsanmeldungen für ein Patent oder eine Erfinderurkunde, und habe auch alle Auslandsanmeldungen für ein Patent oder eine Erfinderurkunde nachstehend gekennzeichnet, die ein Anmeldedatum haben, das vor dem Anmeldedatum der Anmeldung liegt, für die Priorität beansprucht wird.

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

MICRO-RELAY

the specification of which

(check one)

☐ is attached hereto.

☒ was filed on **PCT Filing Date: February 4, 1999**

Application Serial No. **09/890,575**

and was amended on _____

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

German Language Declaration

Prior foreign applications

Priorität beansprucht

Priority Claimed

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

Ich beanspruche hiermit gemäss Absatz 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 120, den Vorzug aller unten aufgeführten Anmeldungen, und falls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in einer früheren amerikanischen Patentanmeldung laut dem ersten Paragraphen des Absatzes 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 112, offenbart ist, erkenne ich gemäss Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung und dem nationalen oder PCT internationalen Anmeldedatum dieser Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

PCT/SG99/00005

(Application Serial No.)
(Anmeldeseriennummer)

February 4, 1999

(Filing Date)
(Anmeldedatum)

pending

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

(Application Serial No.)
(Anmeldeseriennummer)

(Filing Date)
(Anmeldedatum)

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

Ich erkläre hiermit, dass alle von mir in der vorliegenden Erklärung gemachten Angaben nach meinem besten Wissen und Gewissen der vollen Wahrheit entsprechen, und dass ich diese eidesstattliche Erklärung in Kenntnis dessen abgebe, dass wissentlich und vorsätzlich falsche Angaben gemäss Paragraph 1001, Absatz 18 der Zivilprozessordnung der Vereinigten Staaten von Amerika mit Geldstrafe belegt und/oder Gefängnis bestraft werden können, und dass derartig wissentlich und vorsätzlich falsche Angaben die Gültigkeit der vorliegenden Patentanmeldung oder eines darauf erteilten Patentes gefährden können.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

German Language Declaration

VERTRETUNGSVOLLMACHT: Als benannter Erfinder beauftrage ich hiermit den nachstehend benannten Patentanwalt (oder die nachstehend benannten Patentanwälte) und/oder Patent-Agenten mit der Verfolgung der vorliegenden Patentanmeldung sowie mit der Abwicklung aller damit verbundenen Geschäfte vor dem Patent- und Warenzeichenamt: (Name und Registrationsnummer anführen)

Telefongespräche bitte richten an:
(Name und Telefonnummer)

Postanschrift:

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

Salvatore Anastasi (Registration No. 39090),
Michael J. Aronoff (Registration No. 37770),
Stephen J. Driscoll (Registration No. 37564),
Robert J. Kapalka (Registration No. 34198),
Driscoll A. Nina (Registration No. 34685),
Joseph A. Tessari (Registration No. 32177), and
Bruce J. Wolstoncroft (Registration No. 32075).
Direct Telephone Calls to: (name and telephone number)
Robert J. Kapakla (302) 633 2771

Send Correspondence to:

Tyco Technology Resources
4550 New Linden Hill Road
Suite 450
Wilmington, DE 19808

Voller Name des einzigen oder ursprünglichen Erfinders: Dirk WAGENAAR	Full name of third joint inventor, if any: Helmut SCHLAACK
Unterschrift des Erfinders: <i>3-cc</i>	Inventor's signature: <i>Helmut Schlaack</i>
Datum:	Date: <i>7th Nov. 2001</i>
Wohnsitz: 103 Farrer Drive, #01-01, Singapore 259 295	Residence: Potsdamer Str. 67, 64372 Ober-Ramstadt, DEX Germany
Staatsangehörigkeit: German	Citizenship: German
Postanschrift: 103 Farrer Drive, #01-01, Singapore 259 295	Post Office Address: Potsdamer Str. 67, 64372 Ober-Ramstadt, Germany
Voller Name des zweiten Miterfinders, falls zutreffend: Kay KRUPKA	Full name of fourth joint inventor, if any: Uppili SRIDHAR
Unterschrift des Erfinders:	Inventor's signature:
Datum:	Date:
Wohnsitz: Wolkenburgweg 2, 14169 Berlin, Germany	Residence: 8 Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210
Staatsangehörigkeit: German	Citizenship: US
Postanschrift: Wolkenburgweg 2, 14169 Berlin, Germany	Post Office Address: 8 Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterfindern angeben).

(Supply similar information and signature for third and subsequent joint inventors.)

Voller Name des fünften Miterfinders, falls zutreffend:		Full name of seventh joint inventor, if any:	
		Victor D. SAMPER	
Unterschrift des Erfinders:	Datum:	Inventor's signature:	Date:
Wohnsitz:		Residence:	
		38 Dover Rise, Tower A, Dover Parkview #03-12, Singapore 138684	
Staatsangehörigkeit:		Citizenship:	
		British	
Postanschrift:		Post Office Address:	
		38 Dover Rise, Tower A, Dover Parkview #03-12, Singapore 138684	
Voller Name des sechsten Miterfinders, falls zutreffend:		Full name of eighth joint inventor, if any:	
		Pang Dow FOO	
Unterschrift des Erfinders:	Datum:	Inventor's signature:	Date:
Wohnsitz:		Residence:	
		360 Pasir Panjang Road, #03-05, Singapore 118699	
Staatsangehörigkeit:		Citizenship:	
		US	
Postanschrift:		Post Office Address:	
		360 Pasir Panjang Road, #03-05, Singapore 118699	

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterfindern angeben).

(Supply similar information and signature for third and subsequent joint inventors.)

Declaration and Power of Attorney for Patent Application

Erklärung für Patentanmeldungen mit Vollmacht

German Language Declaration

Als nachstehend benannter Erfinder erkläre ich hiermit an Eides Statt:

Dass mein Wohnsitz, meine Postanschrift und meine Staatsangehörigkeit den im nachstehenden nach meinem Namen aufgeführten Angaben entsprechen;

dass ich nach bestem Wissen der ursprüngliche, erste und alleinige Erfinder (falls nachstehend nur ein Name angegeben ist) oder ein ursprünglicher, erster und Miterfinder (falls nachstehend mehrere Namen aufgeführt sind) des Gegenstandes bin, für den dieser Antrag gestellt wird und für den ein Patent beantragt wird für die Erfindung mit dem Titel:

deren Beschreibung

(Zutreffendes ankreuzen)

☐ hier beigefügt ist.

☐ am _____ unter der

Anmeldungsseriennummer _____

eingereicht wurde und am _____
abgeändert wurde (falls tatsächlich abgeändert).

Ich bestätige hiermit, dass ich den Inhalt der obigen Patentanmeldung, einschliesslich der Ansprüche, durchgesehen und verstanden habe, die eventuell durch einen Zusatzantrag, wie oben erwähnt, abgeändert wurde.

Ich erkenne meine Pflicht zur Offenbarung irgendwelcher Informationen, die für die Prüfung der vorliegenden Anmeldung in Einklang mit Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) von Wichtigkeit sind, an.

Ich beanspruche hiermit ausländische Prioritätsvorteile gemäss Abschnitt 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 119 aller unten angegebenen Auslandsanmeldungen für ein Patent oder eine Erfinderurkunde, und habe auch alle Auslandsanmeldungen für ein Patent oder eine Erfinderurkunde nachstehend gekennzeichnet, die ein Anmeldedatum haben, das vor dem Anmeldedatum der Anmeldung liegt, für die Priorität beansprucht wird.

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

MICRO-RELAY

the specification of which

(check one)

☐ is attached hereto.

☒ was filed on **PCT Filing Date: February 4, 1999**

Application Serial No. **09/890,575**

and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

German Language Declaration

Prior foreign applications

Priorität beansprucht

Priority Claimed

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

(Number)
(Nummer)

(Country)
(Land)

Day/Month/Year Filed
Tag/Monat/Jahr eingereicht

☐
Yes
Ja

☐
No
Nein

Ich beanspruche hiermit gemäss Absatz 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 120, den Vorzug aller unten aufgeführten Anmeldungen, und falls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in einer früheren amerikanischen Patentanmeldung laut dem ersten Paragraphen des Absatzes 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 112, offenbart ist, erkenne ich gemäss Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung und dem nationalen oder PCT internationalen Anmeldedatum dieser Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

PCT/SG99/00005

(Application Serial No.)
(Anmeldeseriennummer)

February 4, 1999

(Filing Date)
(Anmeldedatum)

pending

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

(Application Serial No.)
(Anmeldeseriennummer)

(Filing Date)
(Anmeldedatum)

(Status)
(patentiert, anhangig,
aufgegeben)

(Status)
(patented, pending,
abandoned)

Ich erkläre hiermit, dass alle von mir in der vorliegenden Erklärung gemachten Angaben nach meinem besten Wissen und Gewissen der vollen Wahrheit entsprechen, und dass ich diese eidesstattliche Erklärung in Kenntnis dessen abgebe, dass wissentlich und vorsätzlich falsche Angaben gemäss Paragraph 1001, Absatz 18 der Zivilprozessordnung der Vereinigten Staaten von Amerika mit Geldstrafe belegt und/oder Gefängnis bestraft werden können, und dass derartig wissentlich und vorsätzlich falsche Angaben die Gültigkeit der vorliegenden Patentanmeldung oder eines darauf erteilten Patentes gefährden können.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

German Language Declaration

VERTRETUNGSVOLLMACHT: Als benannter Erfinder beauftrage ich hiermit den nachstehend benannten Patentanwalt (oder die nachstehend benannten Patentanwälte) und/oder Patent-Agenten mit der Verfolgung der vorliegenden Patentanmeldung sowie mit der Abwicklung aller damit verbundenen Geschäfte vor dem Patent- und Warenzeichenamt: (Name und Registrationsnummer anführen)

Telefongespräche bitte richten an:
(Name und Telefonnummer)

Postanschrift:

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

Salvatore Anastasi (Registration No. 39090),
Michael J. Aronoff (Registration No. 37770),
Stephen J. Driscoll (Registration No. 37564),
Robert J. Kapalka (Registration No. 34198),
Driscoll A. Nina (Registration No. 34685),
Joseph A. Tessari (Registration No. 32177), and
Bruce J. Wolstoncroft (Registration No. 32075).
Direct Telephone Calls to: (name and telephone number)
Robert J. Kapalka (302) 633 2771

Send Correspondence to:

Tyco Technology Resources
4550 New Linden Hill Road
Suite 450
Wilmington, DE 19808

Voller Name des einzigen oder ursprünglichen Erfinders: Dirk WAGENAAR	Full name of third joint inventor, if any: Helmut SCHLAAK
Unterschrift des Erfinders: _____ Datum: _____	Inventor's signature: _____ Date: _____
Wohnsitz: 103 Farrer Drive, #01-01, Singapore 259 295	Residence: Potsdamer Str. 67, 64372 Ober-Ramstadt, Germany
Staatsangehörigkeit: German	Citizenship: German
Postanschrift: 103 Farrer Drive, #01-01, Singapore 259 295	Post Office Address: Potsdamer Str. 67, 64372 Ober-Ramstadt, Germany
Voller Name des zweiten Miterfinders, falls zutreffend: Kay KRUPKA	Full name of fourth joint inventor, if any: Uppili SRIDHAR
Unterschrift des Erfinders: _____ Datum: <u>4-cc</u>	Inventor's signature: <u>Uppili SRIDHAR</u> Date: <u>23/11/2001</u>
Wohnsitz: Wolkenburgweg 2, 14169 Berlin, Germany	Residence: 8 Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210 SGX
Staatsangehörigkeit: German	Citizenship: US
Postanschrift: Wolkenburgweg 2, 14169 Berlin, Germany	Post Office Address: 8 Woodgrove Condominium, #01-32, Woodgrove Drive, Singapore 738210

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterfindern angeben).

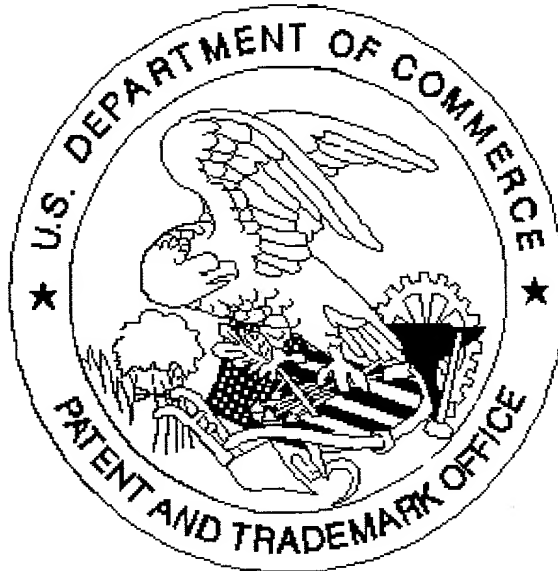
(Supply similar information and signature for third and subsequent joint inventors.)

Voller Name des fünften Miterfinders, falls zutreffend:		Full name of seventh joint inventor, if any:	
5-00		Victor D. SAMPER	
Unterschrift des Erfinders:	Datum:	Inventor's signature:	Date:
		<i>VSamper</i>	5/9/01
Wohnsitz:		Residence:	
		38 Dover Rise, Tower A, Dover Parkview #03-12, Singapore 138684 SGX	
Staatsangehörigkeit:		Citizenship:	
		British	
Postanschrift:		Post Office Address:	
		38 Dover Rise, Tower A, Dover Parkview #03-12, Singapore 138684	
Voller Name des sechsten Miterfinders, falls zutreffend:		Full name of eighth joint inventor, if any:	
6-00		Pang Dow FOO	
Unterschrift des Erfinders:	Datum:	Inventor's signature:	Date:
		<i>Pang Dow Foo</i>	10/4/2001.
Wohnsitz:		Residence:	
		360 Pasir Panjang Road, #03-05, Singapore 118699 SGX	
Staatsangehörigkeit:		Citizenship:	
		US	
Postanschrift:		Post Office Address:	
		360 Pasir Panjang Road, #03-05, Singapore 118699	

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterfindern angeben).

(Supply similar information and signature for third and subsequent joint inventors.)

United States Patent & Trademark Office
Office of Initial Patent Examination -- Scanning Division



Application deficiencies found during scanning:

☐ Page(s) _____ of _____ were not present
for scanning. (Document title)

☐ Page(s) _____ of _____ were not present
for scanning. (Document title)

☐ *Scanned copy is best available. drawing are done*

2011-02-15 15:06:00